

To: Document Control Officer		NXP Semiconductors Austria GmbH Styria	
Mikron-Weg 1, A-8101 Gratkorn, Austria		Fax: +43 3124 299 124 Email: nxp.docu-control@nxp.com Date:	
From: Please note: passwords cannot be provided if data is not typed or missing			
Person's name:	Company's name:	Fax:	e-mail:
Project information: (Please provide additional project information for which UCODE product documentation is required):			
End Application:			
System Integrator:			
Label Supplier:			
Project Schedule:			
Volume First Year:			
Total Volume:			
Any Support needed?			
Comments:			

Dear NXP Document Control Officer, please send the documents of the following UCODE products to my address as stated above.

UCODE G2X Documentation		Comment
<input type="checkbox"/>	UCODE G2X Documentation Package	Includes all below documents
<input type="checkbox"/>	SL3ICS1002/1202 UCODE G2XM/G2XL Functional Spec.	G2XM/G2XL Datasheet (1390)
<input type="checkbox"/>	UCODE G2XM/G2XL Product short data sheet	G2X Short DS (1573)
<input type="checkbox"/>	General Packing Specification Flip Chip Strap – FCS2	Document 1738
<input type="checkbox"/>	General specification for 8" wafer on UV-tape with electronic fail die marking	Document 1093

UCODE HSL Documentation		Comment
<input type="checkbox"/>	UCODE HSL Documentation Package	Includes all below documents
<input type="checkbox"/>	SL3ICS3001 UCODE HSL Functional Specification	HSL Datasheet (0728)
<input type="checkbox"/>	SL3S3001FTT TSSOP8 Package Specification	Document 0760
<input type="checkbox"/>	SL3ICS3001 Bumped Wafer Specification	Document 0707

UCODE EPC Gen2 Documentation		Comment
<input type="checkbox"/>	UCODE EPC Gen2 Documentation Package	Includes all below documents
<input type="checkbox"/>	SL3ICS10 UCODE EPC Gen2 Datasheet	Gen2 Datasheet (1099)
<input type="checkbox"/>	SL3S1001FTT EPC Gen2 TSSOP8 Package Spec	Document 1122
<input type="checkbox"/>	SL3ICS1001FW EPC Gen2 Bumped Wafer Spec	Document 1124

UCODE Application Notes		Comment
<input type="checkbox"/>	General UHF RFID Label Antenna Design	Document 1629
<input type="checkbox"/>	General Far Field Antenna Design Guide	Document 0971
<input type="checkbox"/>	Starter Guide – PCB Tagging	Document 1847
<input type="checkbox"/>	Package and Assembly Guideline for the UCODE G2X	Document 0968
The following documents are available if the Reference Antenna Design Agreement section of this document is additionally signed.		
<input type="checkbox"/>	UCODE G2X Antenna Reference Design Package	Includes all below documents
<input type="checkbox"/>	Far Field Reference Antenna Design for UCODE G2X	Document 0969
<input type="checkbox"/>	Fashion Reference Antenna Design for the G2X	Document 1538
<input type="checkbox"/>	General Purpose Reference Antenna Design for G2X	Document 0972
<input type="checkbox"/>	Mid Range Reference Antenna Design for the G2X	Document 1523
<input type="checkbox"/>	Reference Antenna Design Short Range Ring G2X	Document 1615
<input type="checkbox"/>	UHF RFID Reference Antenna Design for PCBs	Document 1715
<input type="checkbox"/>	Airline Luggage Tag Reference Antenna Design G2X	Document 1685

I accept that NXP Semiconductors has the right to reject this request without giving reasons. I understand and accept that the CONFIDENTIAL marked documents are confidential and that I will handle those according to the following provisions:

1. Recipient undertakes not to disclose any data and information of the above mentioned documents, which Recipient has received or will receive – in whatsoever form – from NXP to any third party.
2. Recipient and NXP agree that the above stated obligations under this Agreement shall not apply to any data and information which, shown by documented proof, (a) now are or later enter the public domain, (b) Recipient receives from a third party without breach of a confidentiality obligation, or (c) were at the time of disclosure already in the possession of Recipient.
3. In respect of the disclosed data and information NXP reserves right and title to apply for juridical protection. The disclosure for the Recipient does not imply any right or title to apply for a protection on its side. The disclosure does not include the granting of any licence.
4. Recipient is responsible that its employees – as far as they are getting knowledge of the received data and information or having the possibility to get to know them – are or shall be committed according to these provisions respectively. The obligations of Recipient under this Agreement remain, however, untouched therefrom.
5. Recipient agrees to promptly return to NXP on demand any data and information which were furnished under this Agreement in written or any other material form, without retaining any copies.

 Signature

 Place, Date

Reference Antenna Design Agreement

This agreement governs the use of NXP reference antenna designs between the user of the designs (hereinafter referred to as “the customer”) and NXP Semiconductors Austria GmbH Styria.

The customer hereby agrees to the following conditions:

- The UHF reference antennas designed by NXP shall only be assembled with NXP UCODE devices.
- No licensing charge applies when using NXP UHF reference antennas with NXP UCODE devices.
- NXP Semiconductors does not indemnify on antenna IP.
- General terms and conditions apply, see <http://www.nxp.com/profile/terms/austria/>
- The design(s), specifications, documentation and other data provided herewith (collectively the “Reference Design”) are provided by NXP for use with NXP products. This Reference Design is provided by NXP as an accommodation to user, and on an “as-is” basis. All warranties, representations, or guarantees of any kind with respect to the Reference Design (whether express, implied or statutory) including, without limitation, warranties of merchantability, fitness for a particular purpose, title, or non-infringement, are specifically disclaimed. Without limiting the generality of the foregoing, NXP does not warrant the functionality of the Reference Design for any particular use. Nor does NXP warrant that the Reference Design is error-free, tested, or will function in the manner represented. It is the user’s sole responsibility to determine whether the Reference Design is suitable for the user’s intended use. It is also user’s sole responsibility to determine whether the Reference Design, or any portion thereof, infringes patents, copyrights or other intellectual property rights. No contractual obligations whatsoever are formed, either directly or indirectly, by NXP making this Reference Design available for user’s consideration, or by user’s ultimate use of the Reference Design. By providing this Reference Design, NXP does not grant a license, express, implied, by estoppel or otherwise, to NXP’s or any third party’s intellectual property rights.

Signature

Place, Date