

# Mextram 504

## experimental results

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Willy Kloosterman

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Research



**PHILIPS**

- Status of Mextram 504
- Short overview of changes for Mextram 504
- Main results with Mextram 504

## Changes in Mextram 504

(2)

**Smoother epilayer modeling (1 new parameter)**

See CMC Dec '99

**Decoupling of DC/AC/scaling modeling (4)**

Charge parameters decoupled from DC

Scaling parameters decoupled from DC

Introduction of more transit time parameters

**SiGe features (2)**

**Self-heating (2)**

**Thorough review of the model (8 removed, 9 new)**

Unnecessary details removed

Avalanche model rewritten and decoupled

Temperature model rewritten

Single Poly BiCMOS process

Emitter size:  $0.6 \times 5.4 \mu\text{m}^2$

Double base contact:  $\rho_{\square} = 10 \text{ k}\Omega$

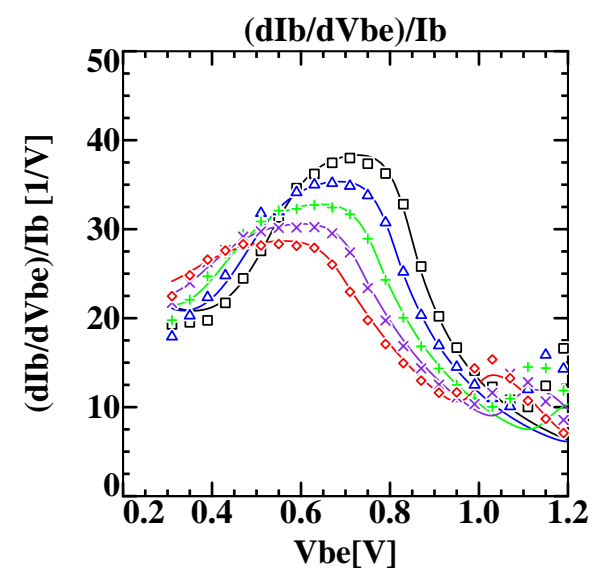
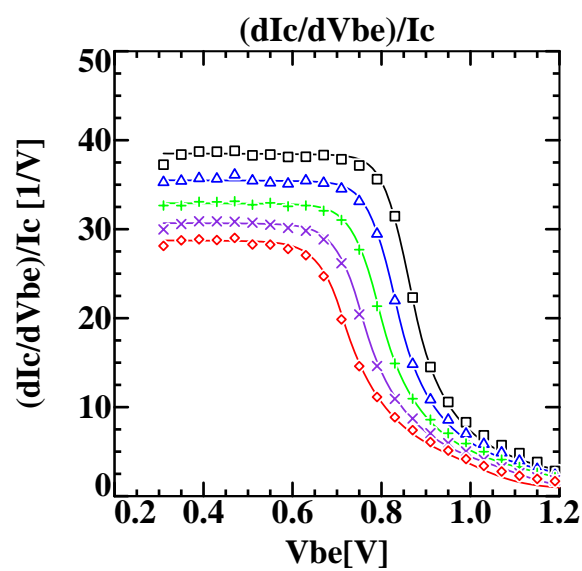
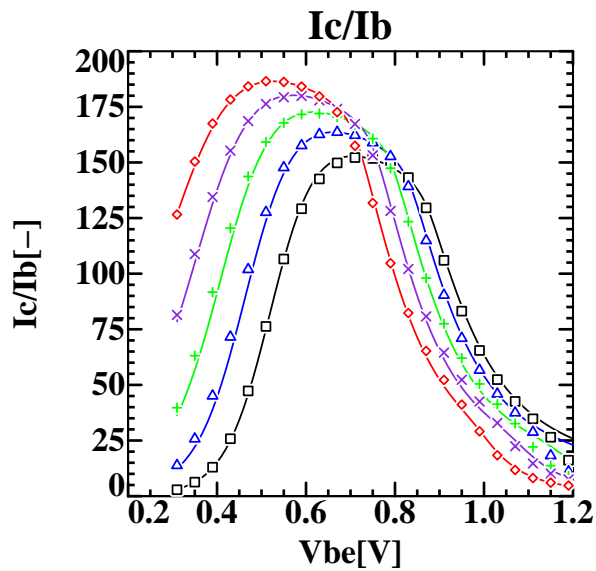
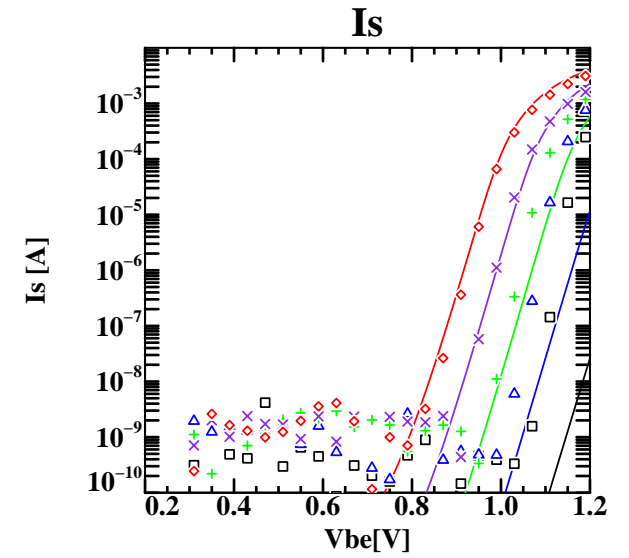
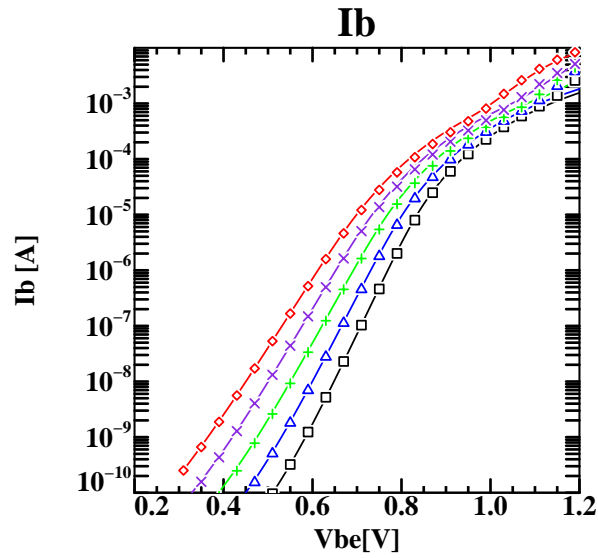
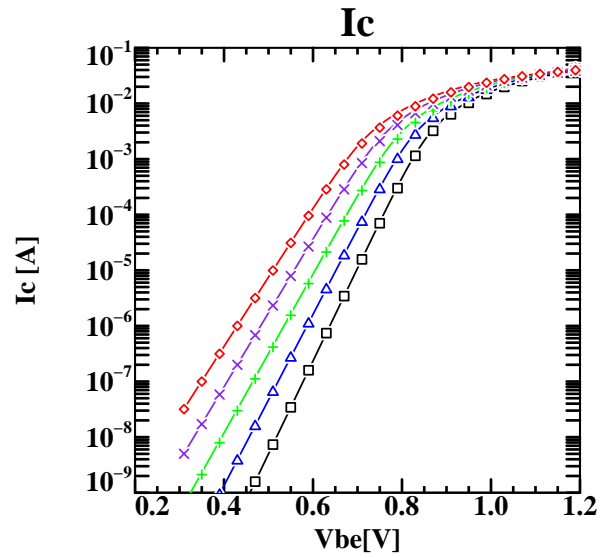
Maximum cut off frequency  $f_T$

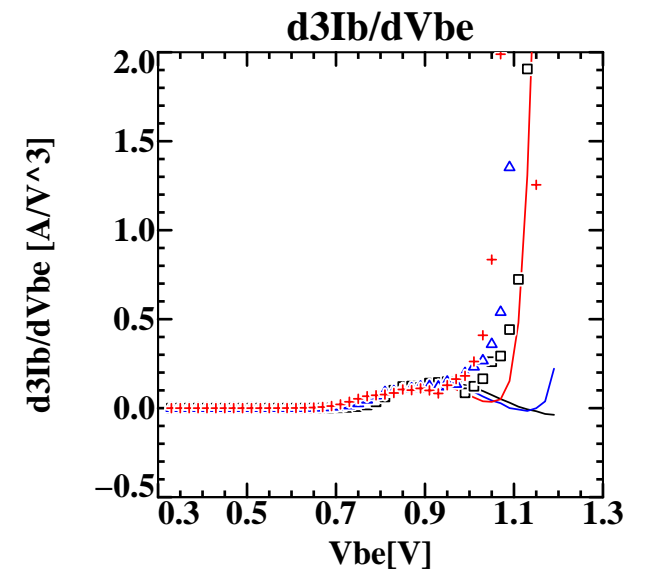
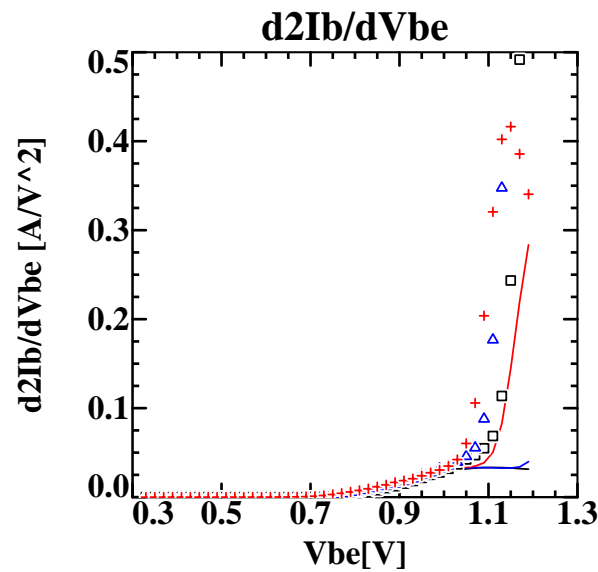
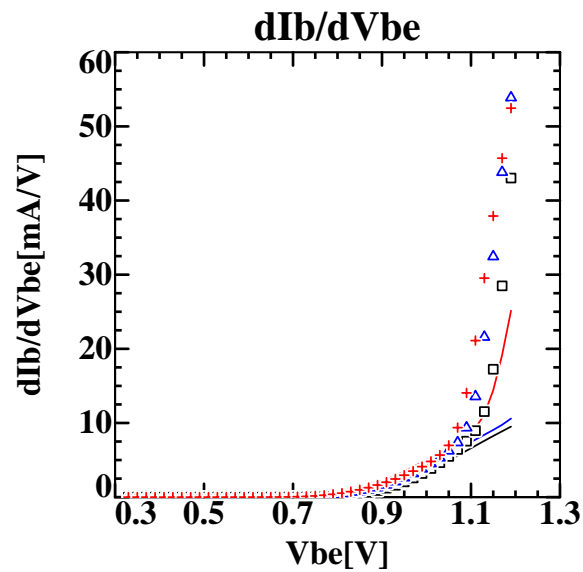
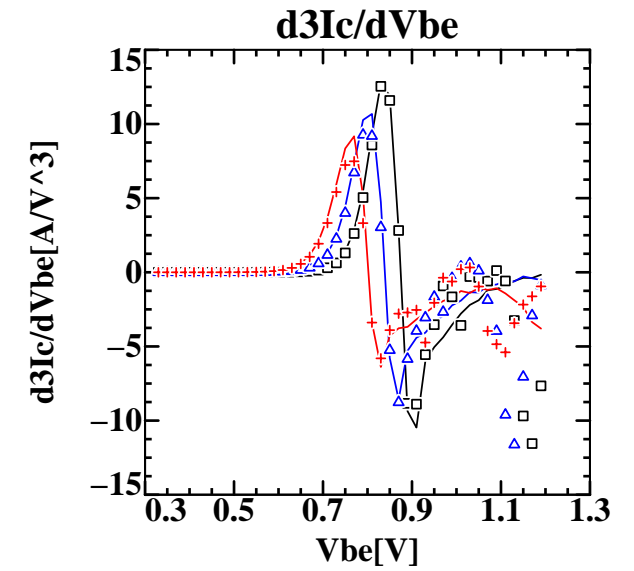
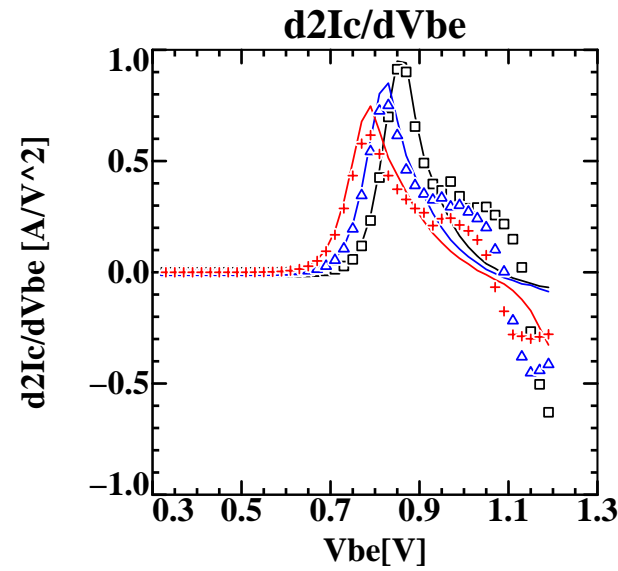
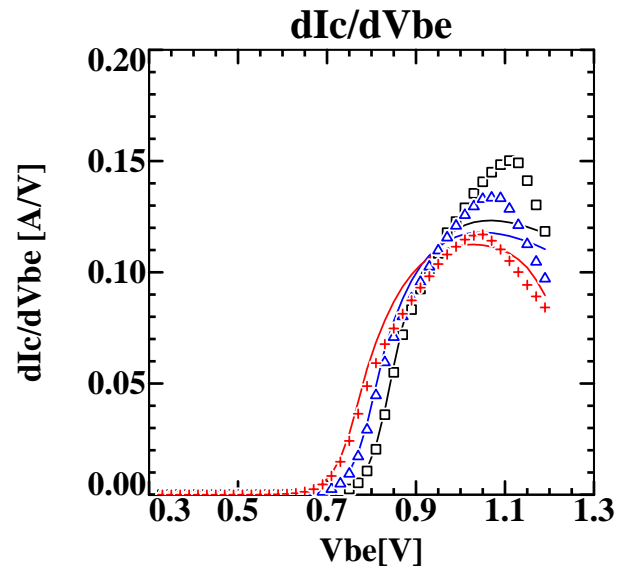
$f_T$ : 10 GHz @  $V_{CE} = 5 \text{ V}$

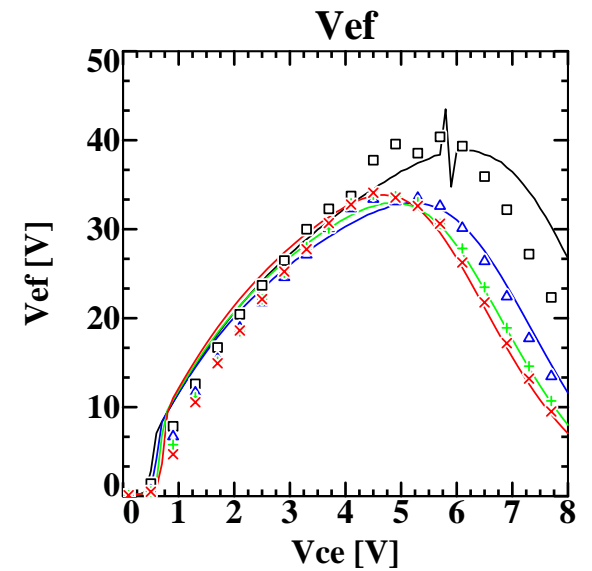
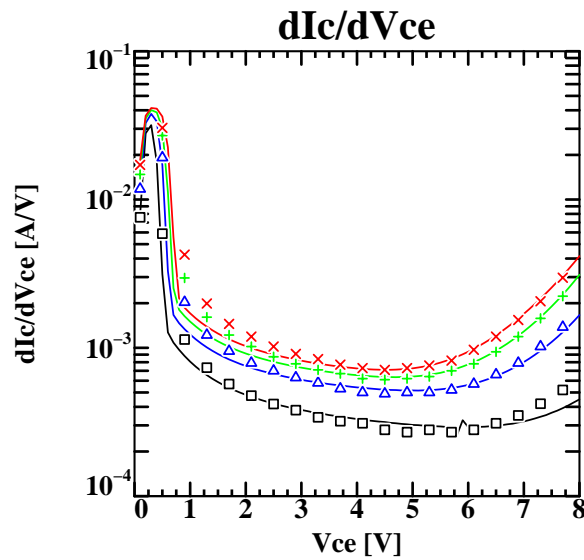
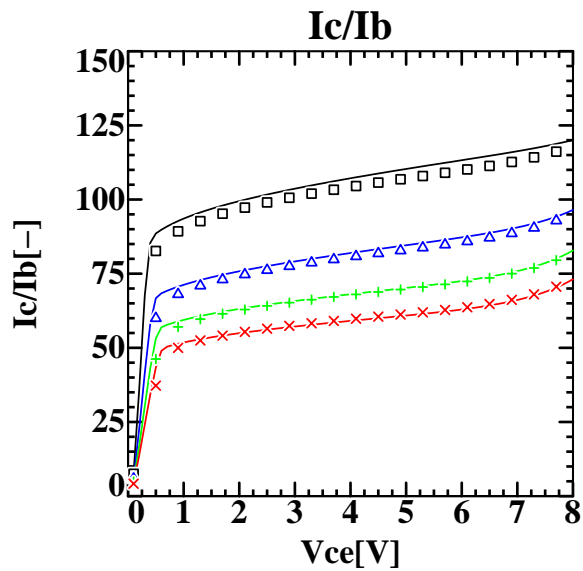
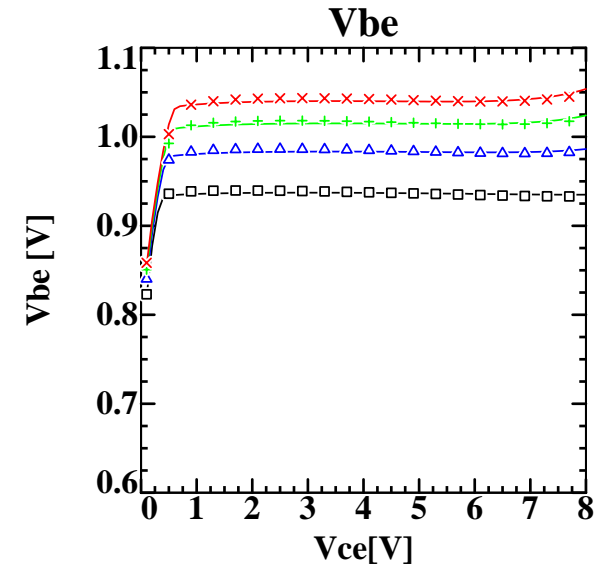
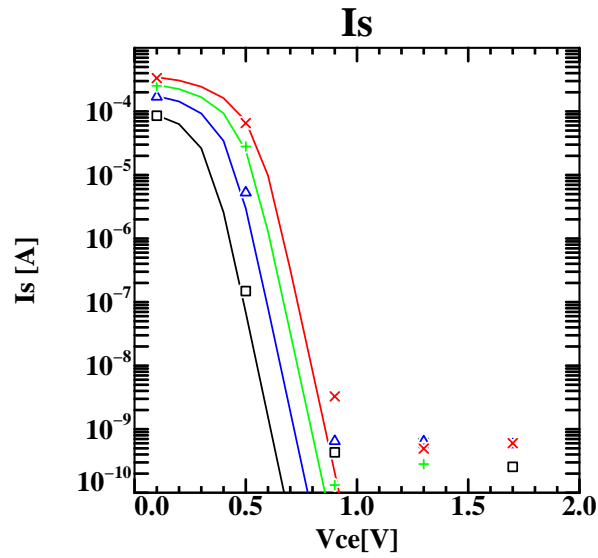
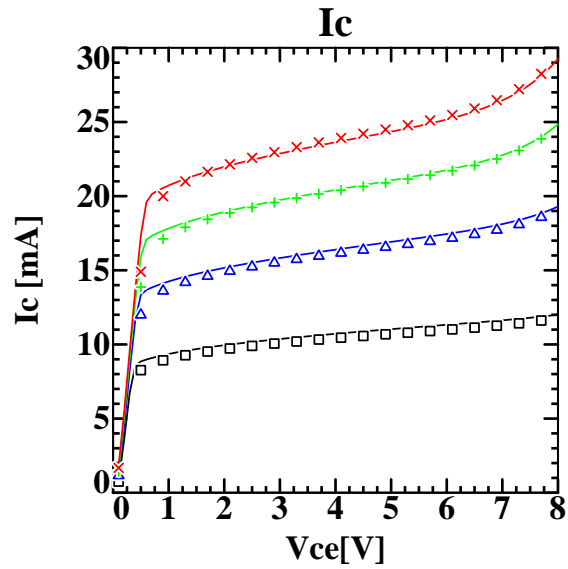
$f_T$ : 6 GHz @  $V_{CE} = 0.5 \text{ V}$

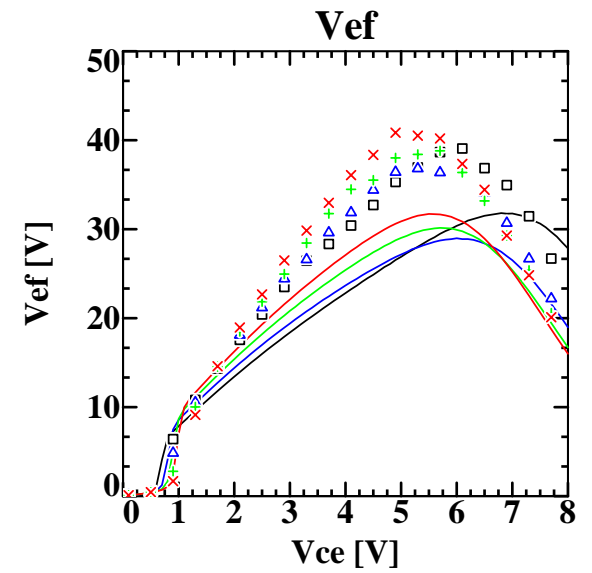
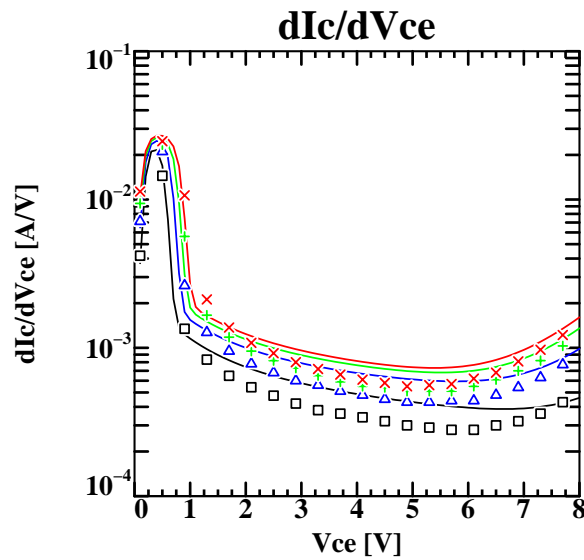
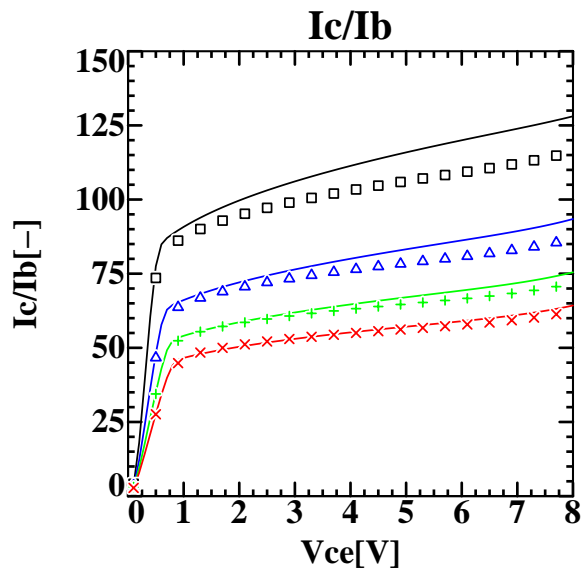
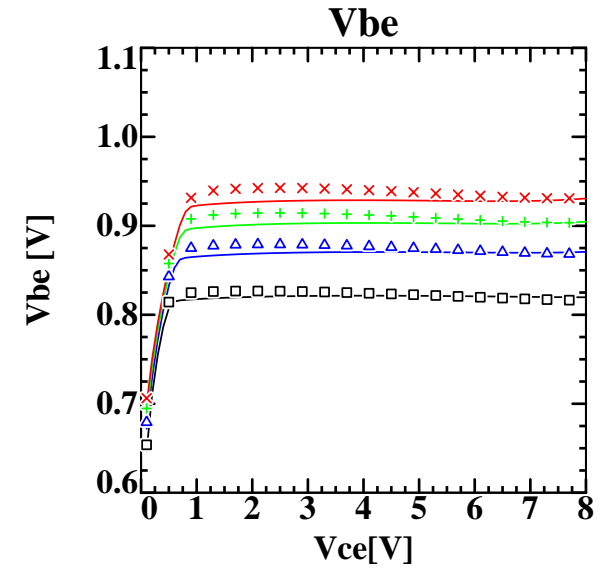
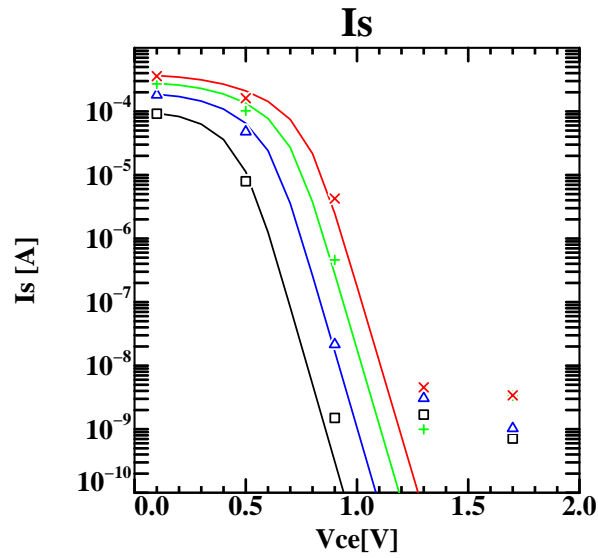
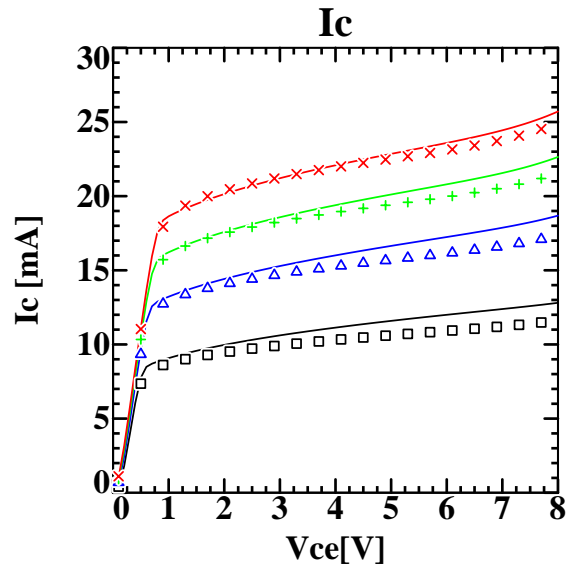
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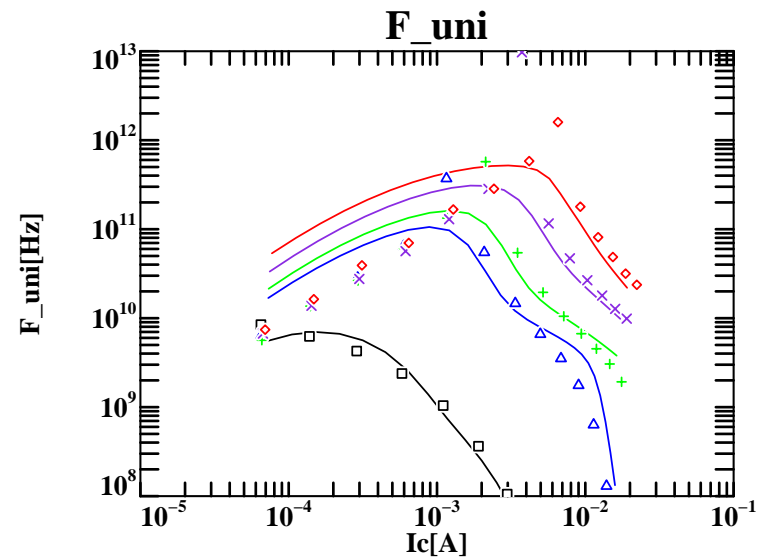
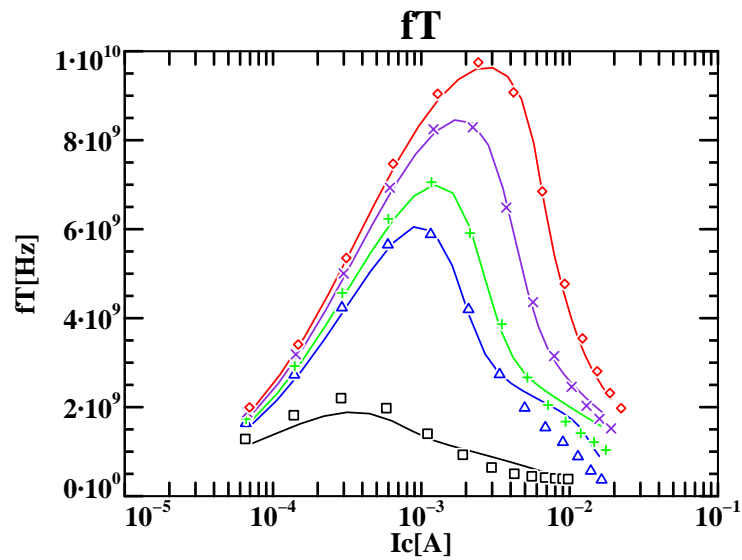
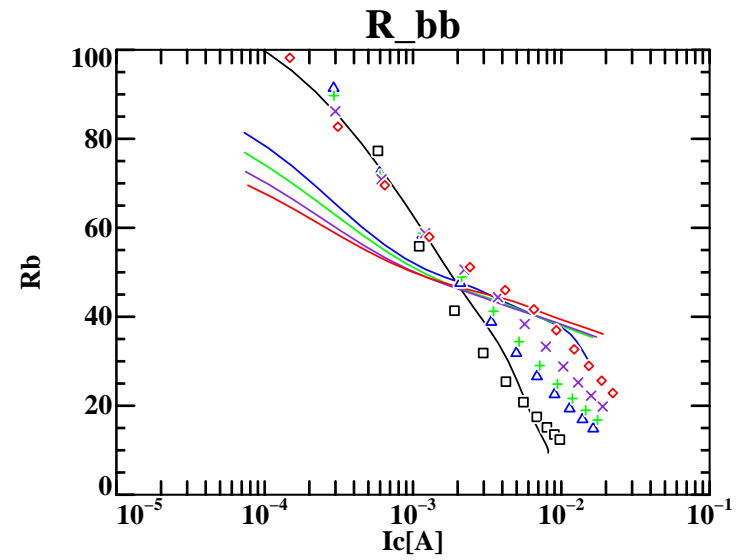
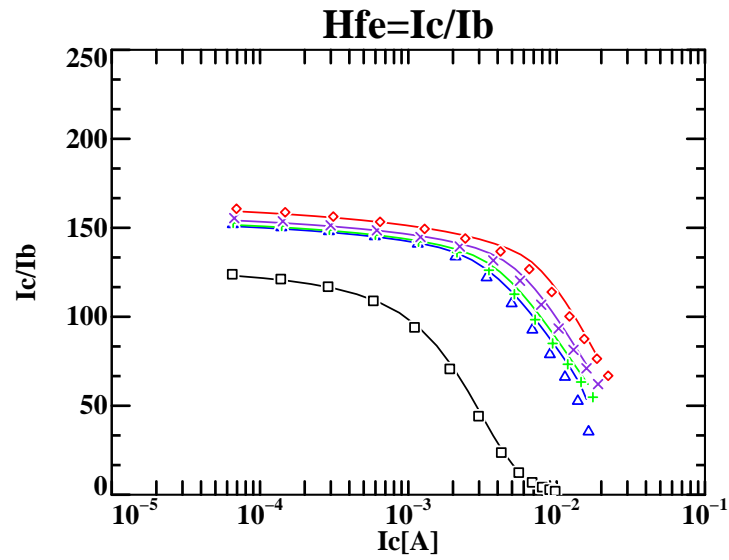
Thick and low-doped epilayer

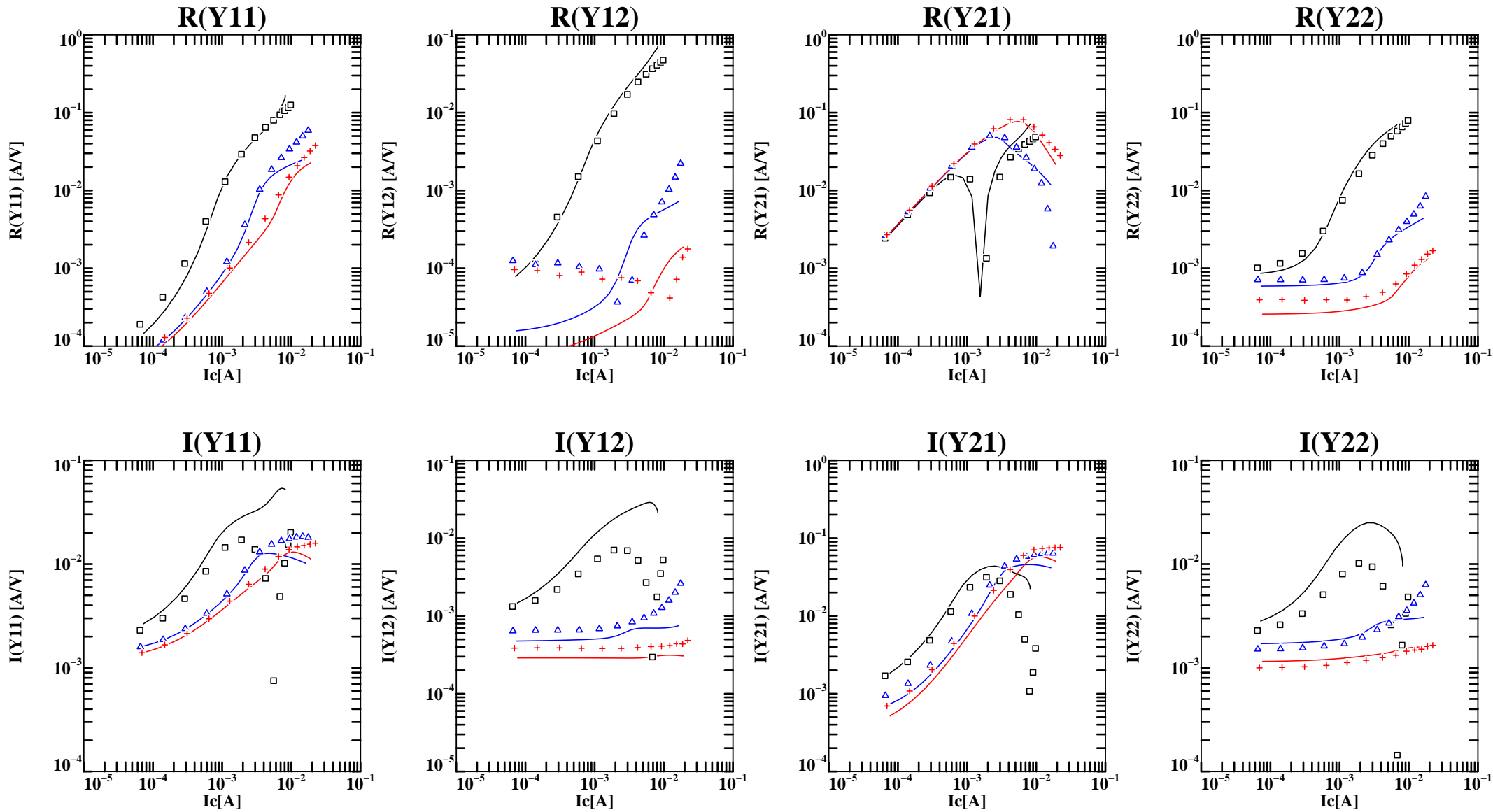












SiGe BiCMOS process

Emitter size:  $0.25 \times 5.75 \mu\text{m}^2$

Single base contact:  $\rho_{\square} = 6.5 \text{ k}\Omega$

Maximum cut off frequency  $f_T$

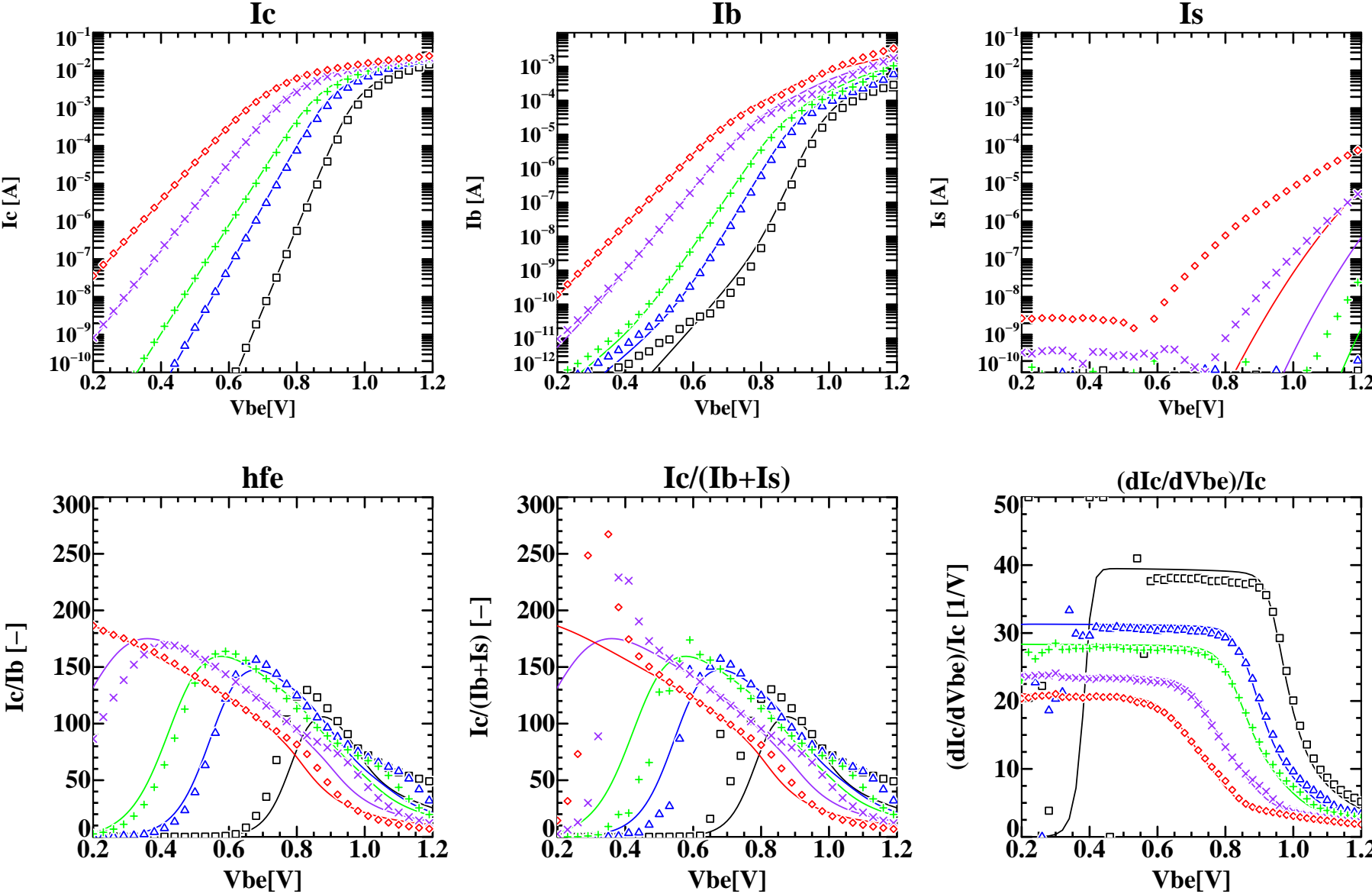
$f_T$ : 97 GHz @  $V_{CE} = 2 \text{ V}$

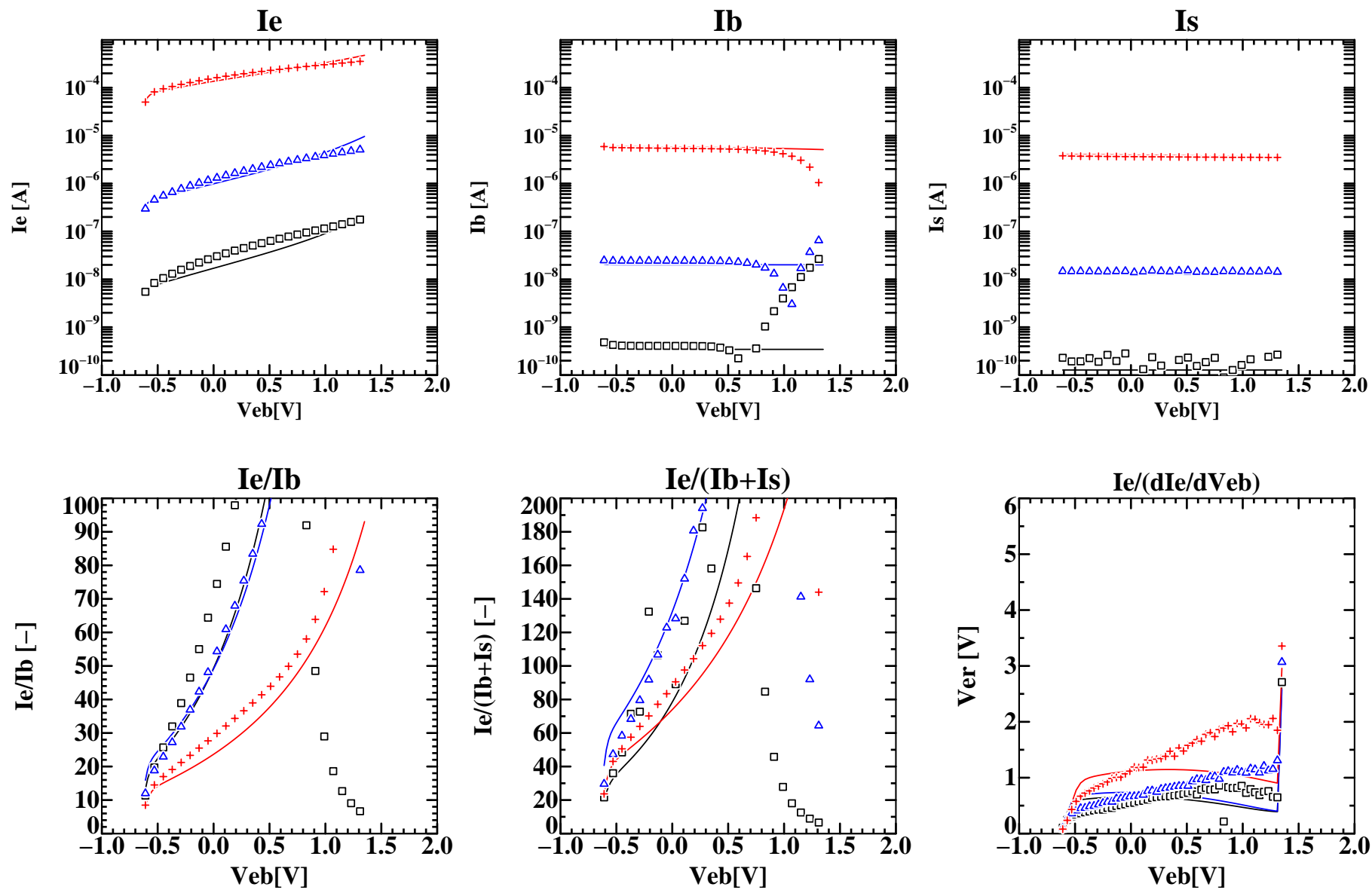
$f_T$ : 85 GHz @  $V_{CE} = 0.75 \text{ V}$

Problem for modeling:

Graded Ge concentration

→ Difficult to model reverse Early effect  
both in forward and in reverse





## Graded Ge-profile modeling

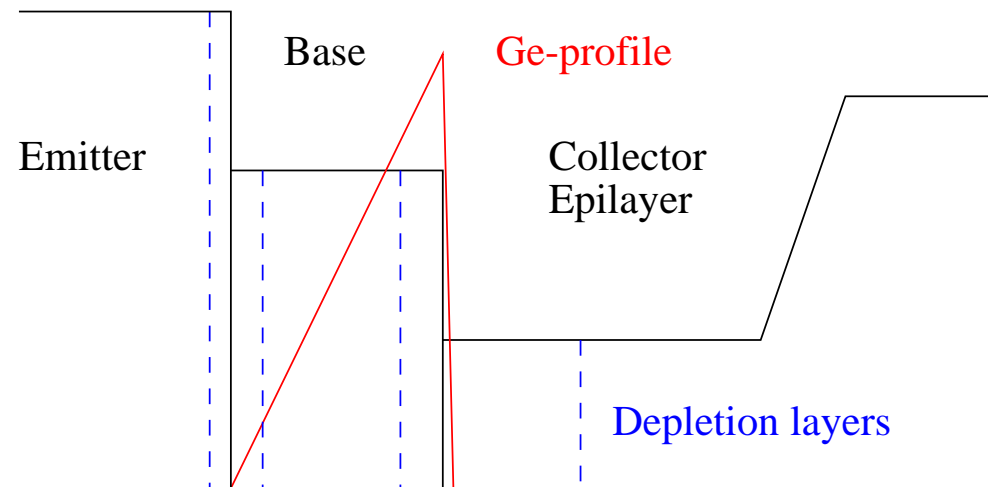
(13)

→ High forward Early voltage

→ Low reverse Early voltage

For reverse output characteristics this leads to punchthrough.

Extra model feature, based on 'ideal' profiles in base



## Graded Ge-profile modeling

(14)

Main current:

$$I_C \simeq I_S e^{V_{BE}/V_T} / q_B$$

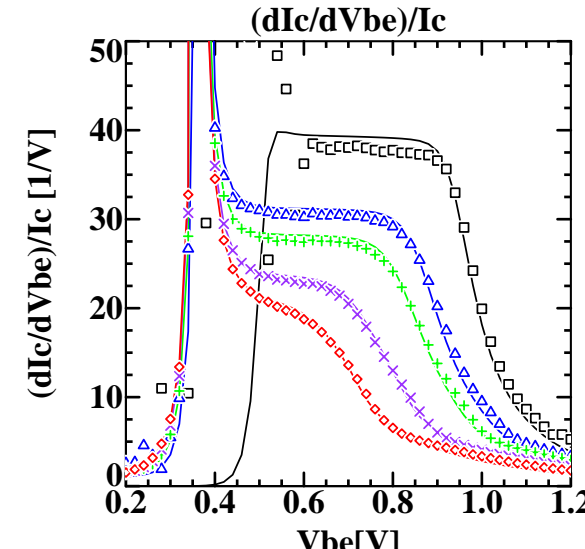
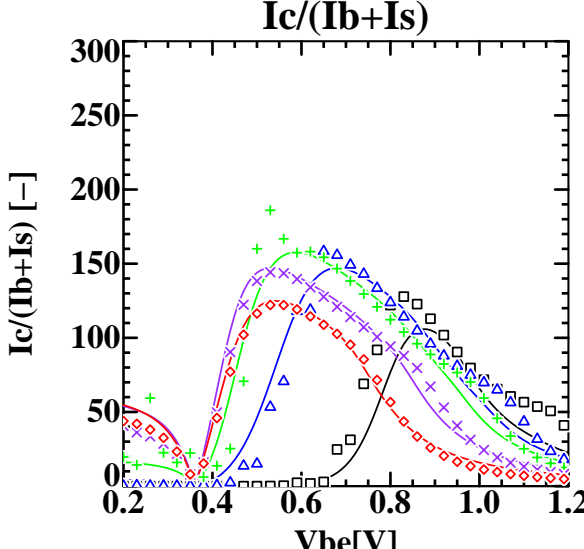
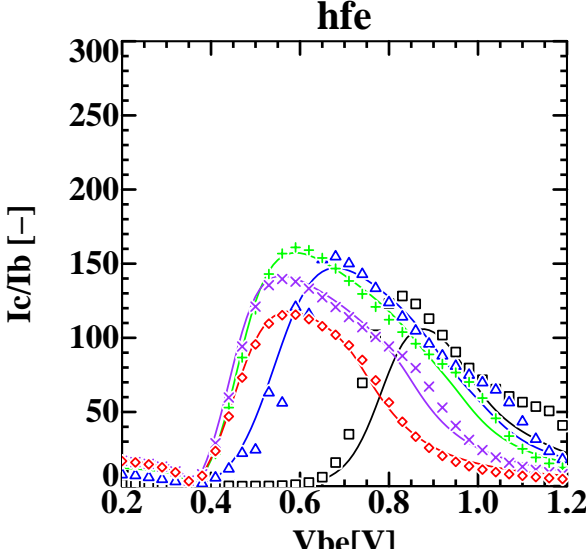
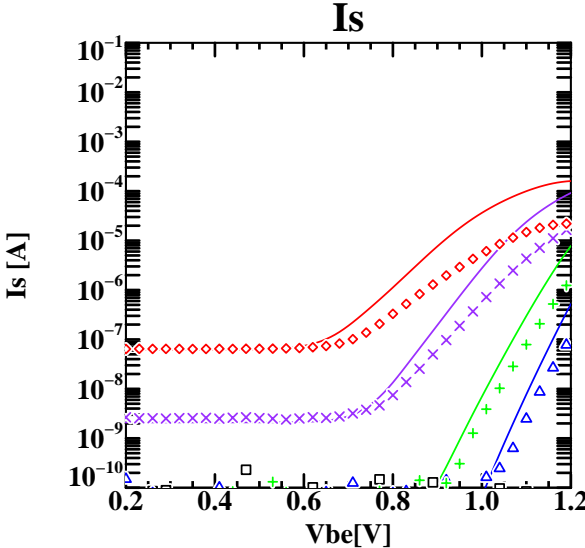
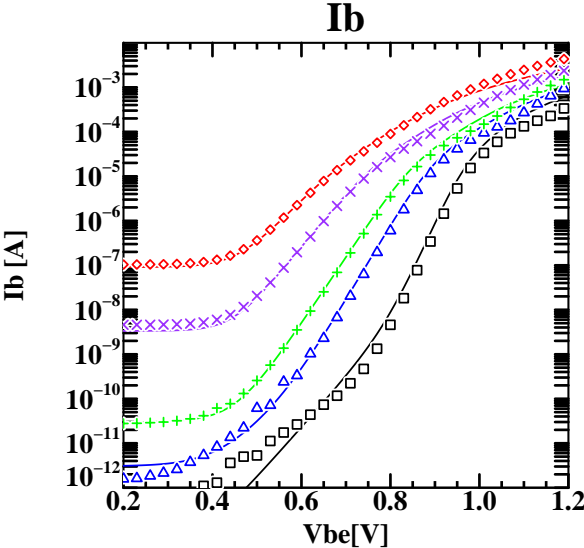
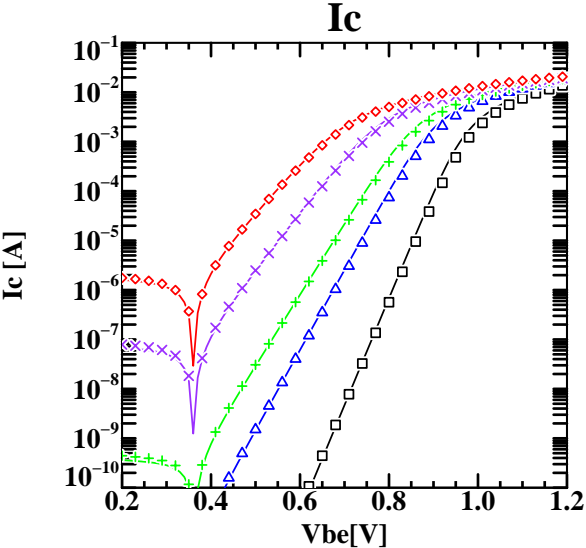
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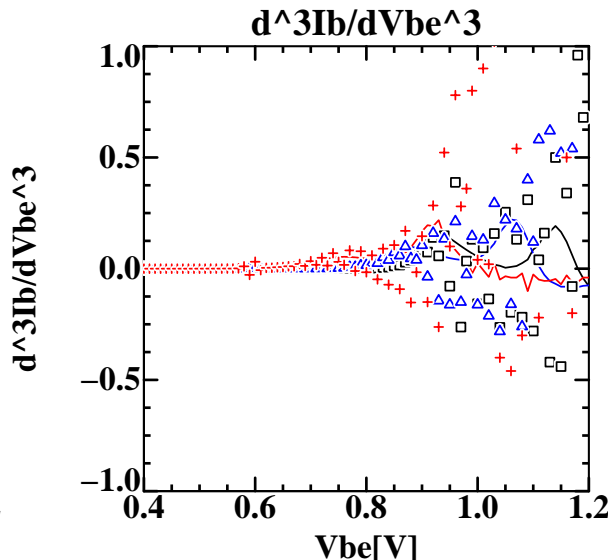
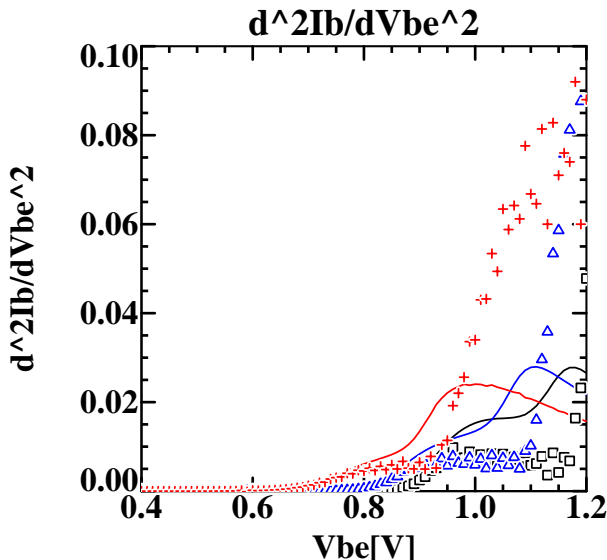
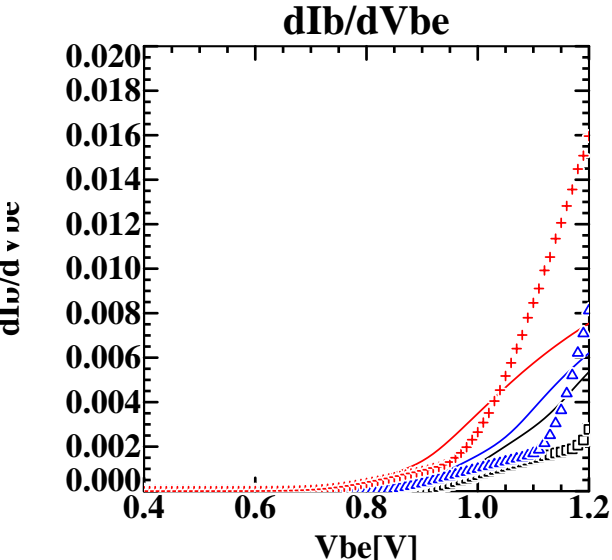
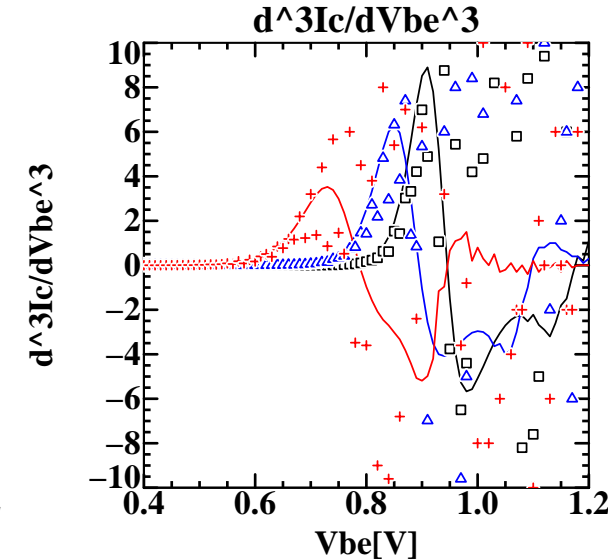
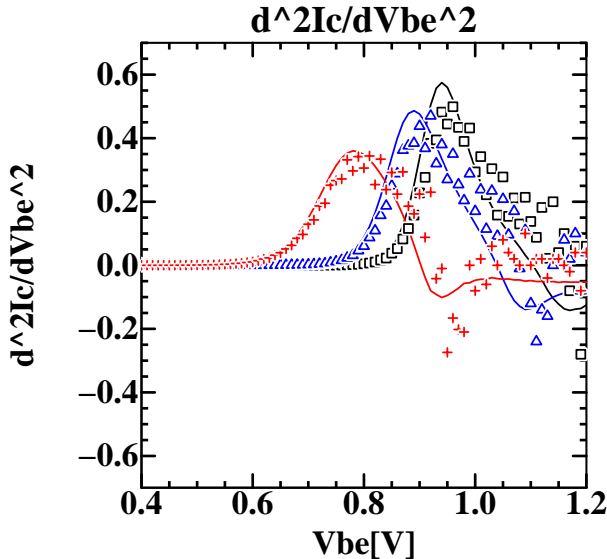
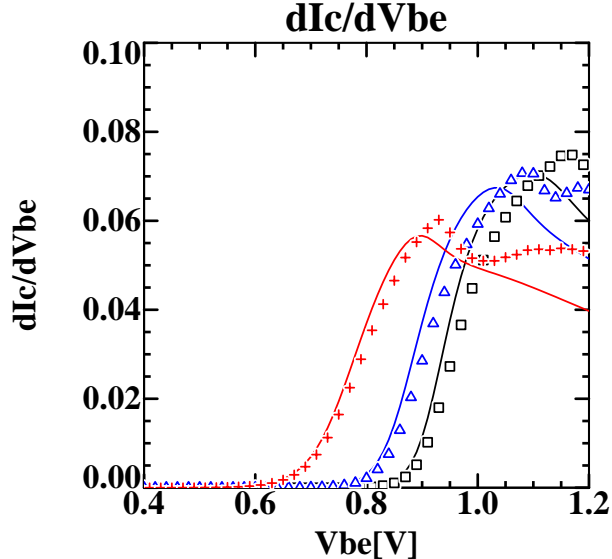
$$q_B \simeq 1 + \frac{V_{BE}}{V_{ER}} + \frac{V_{BC}}{V_{EF}}$$

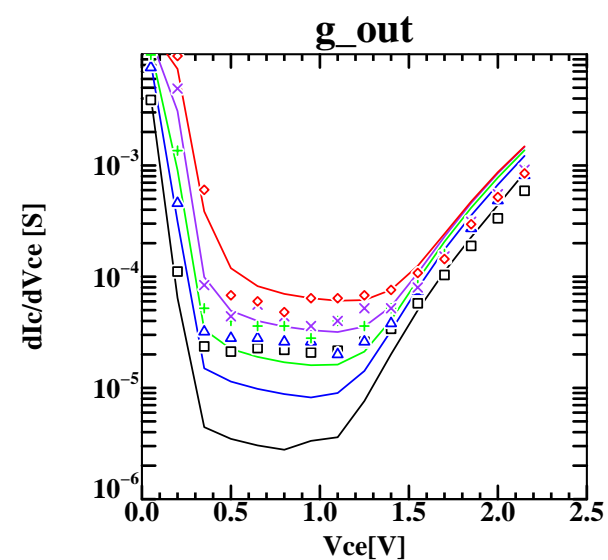
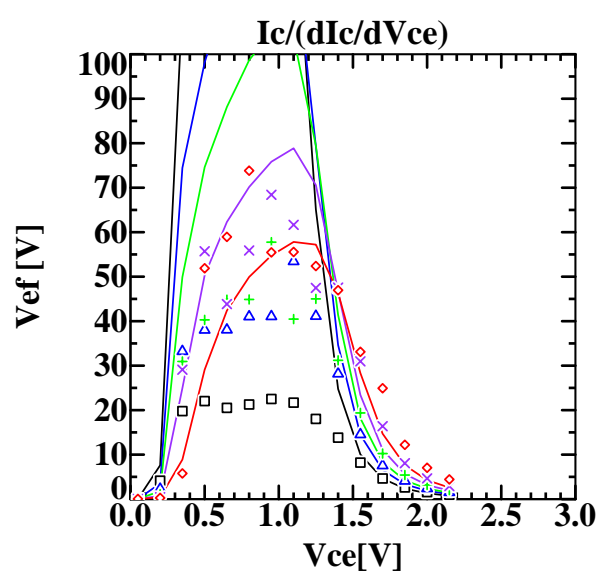
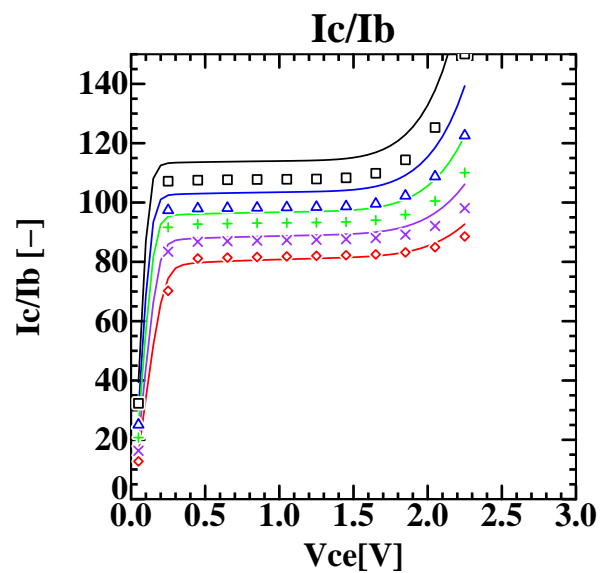
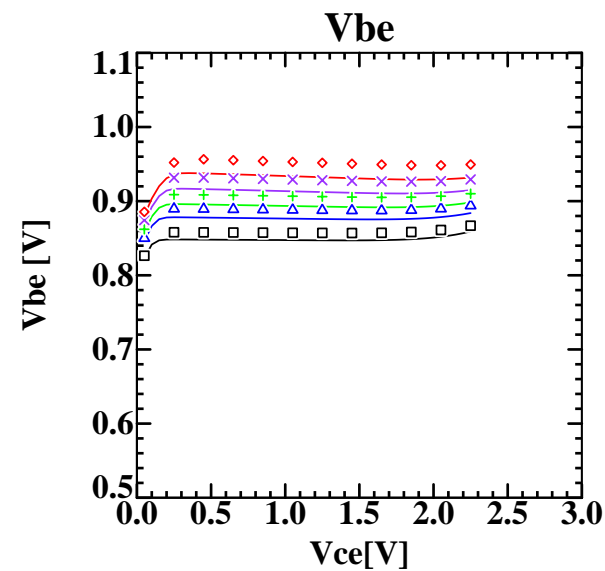
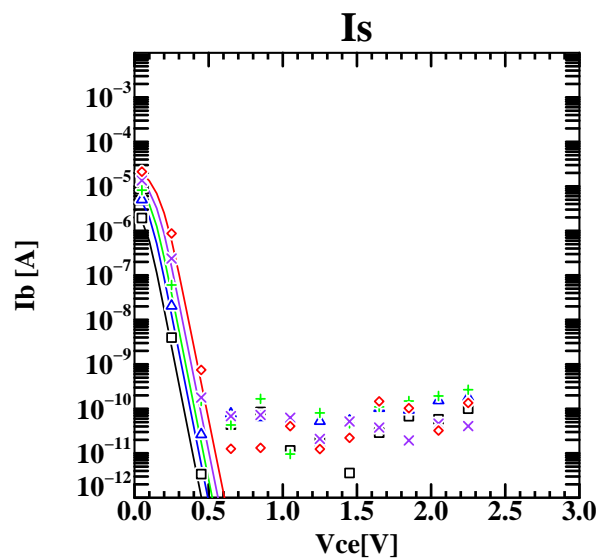
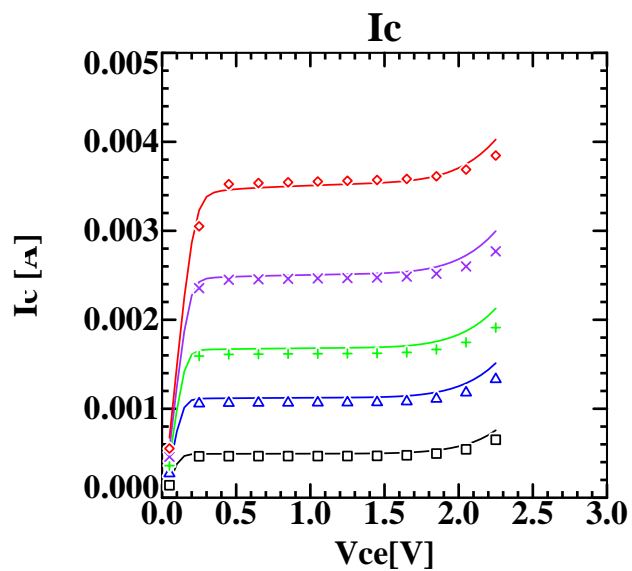
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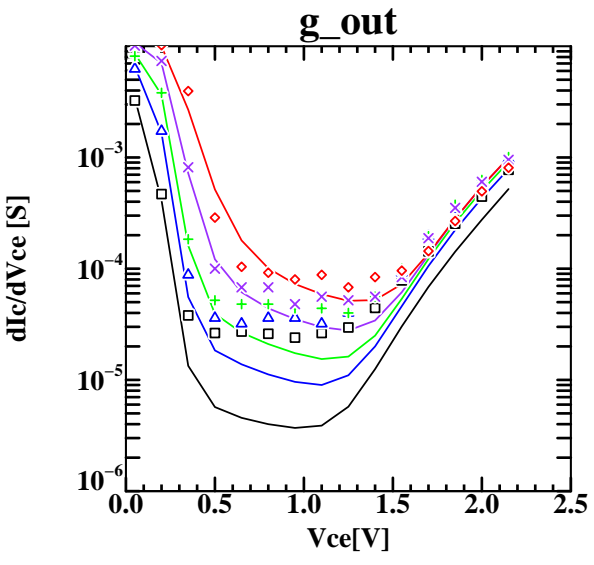
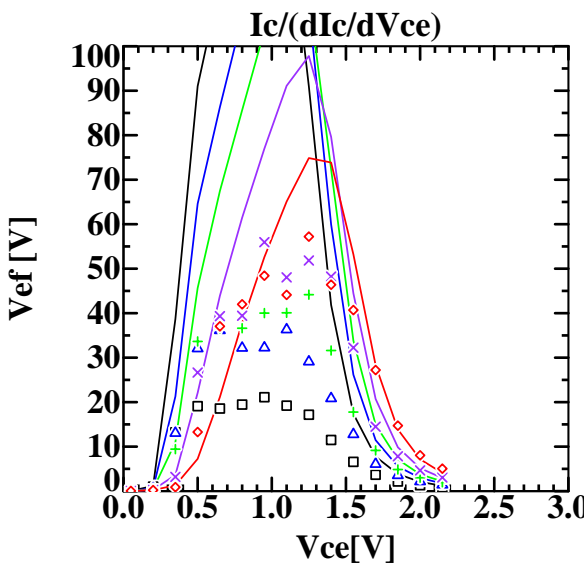
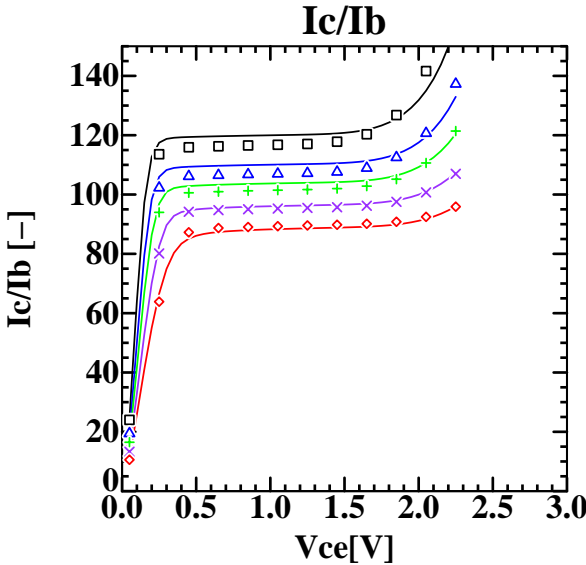
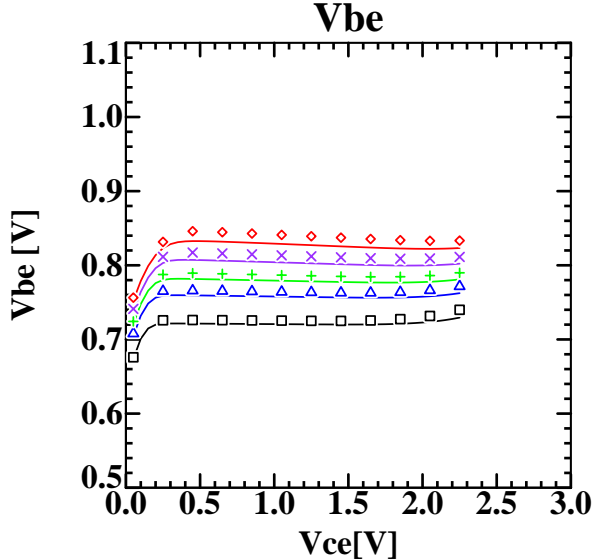
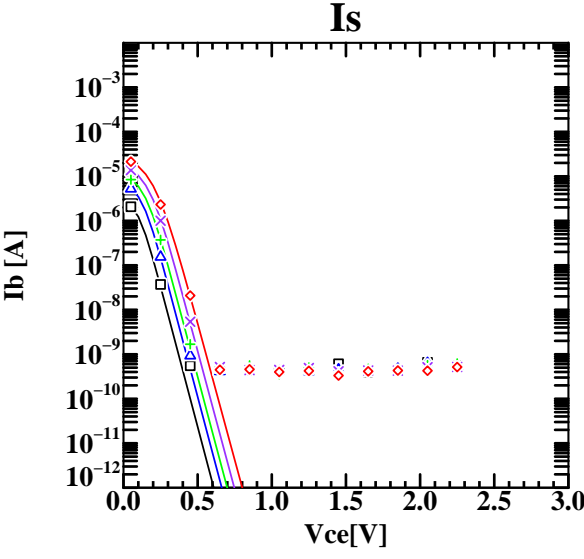
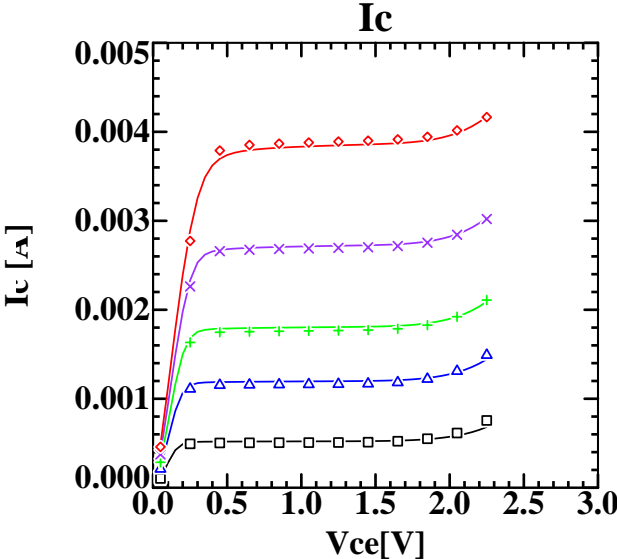
$$q_B \simeq \frac{\exp \left[ \left( 1 + \frac{V_{BE}}{V_{ER}} \right) \frac{dV}{V_T} \right] - \exp \left[ -\frac{V_{BC}}{V_{EF}} \frac{dV}{V_T} \right]}{\exp \left[ \frac{dV}{V_T} \right] - 1}$$

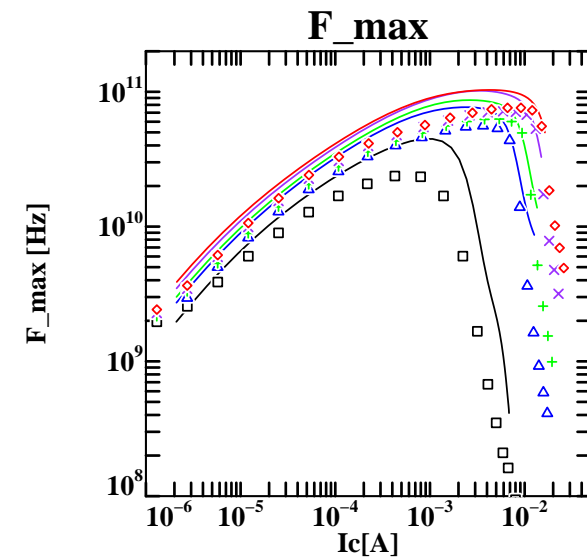
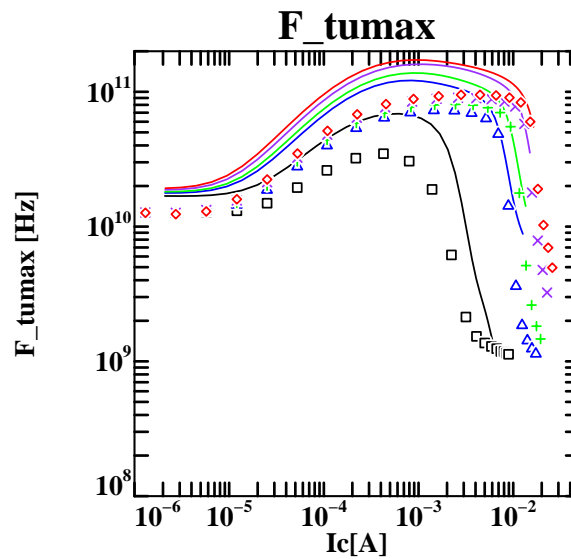
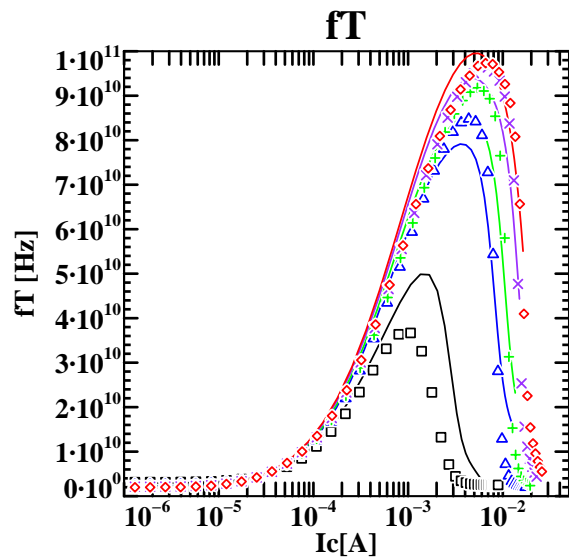
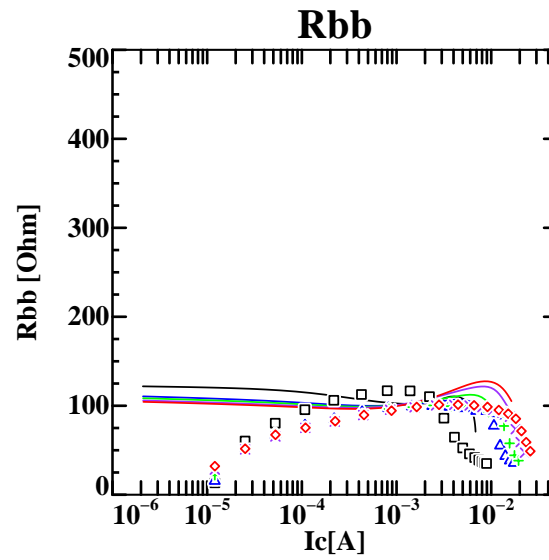
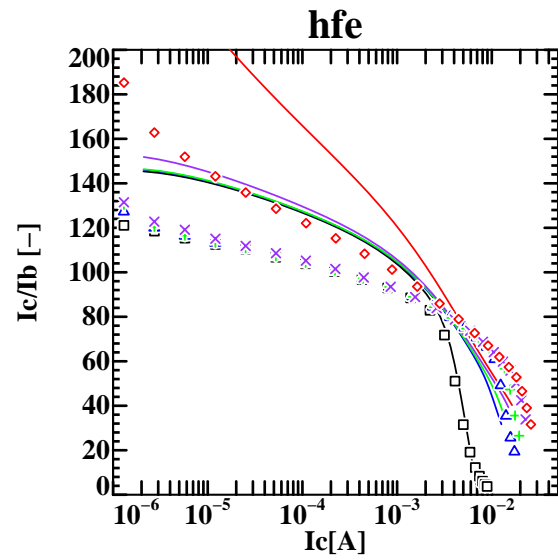
Parameter:  $dV$ : band-gap narrowing over neutral base

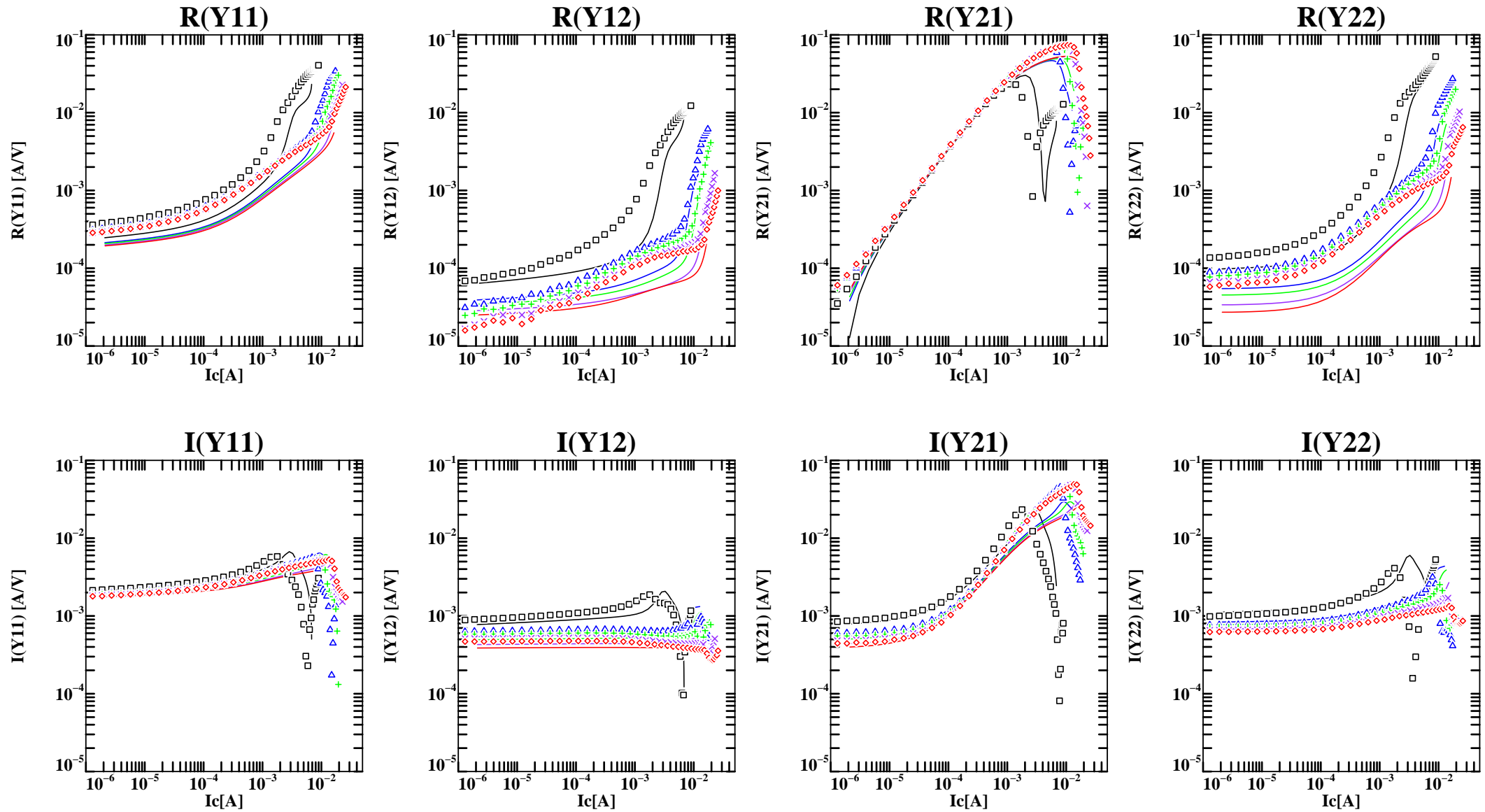












NPN on SOI

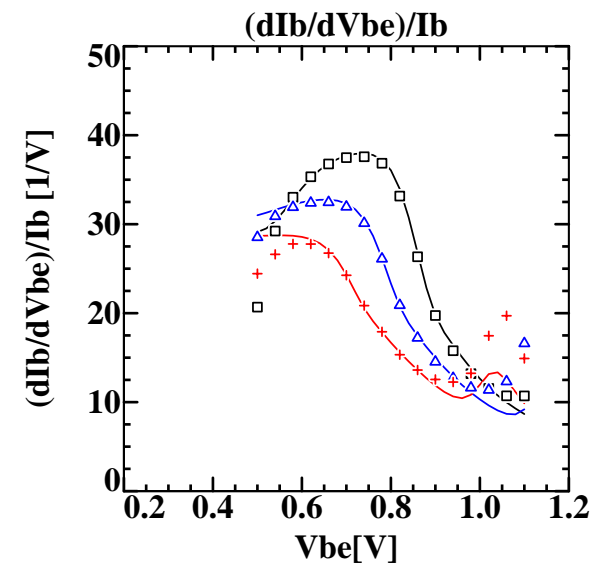
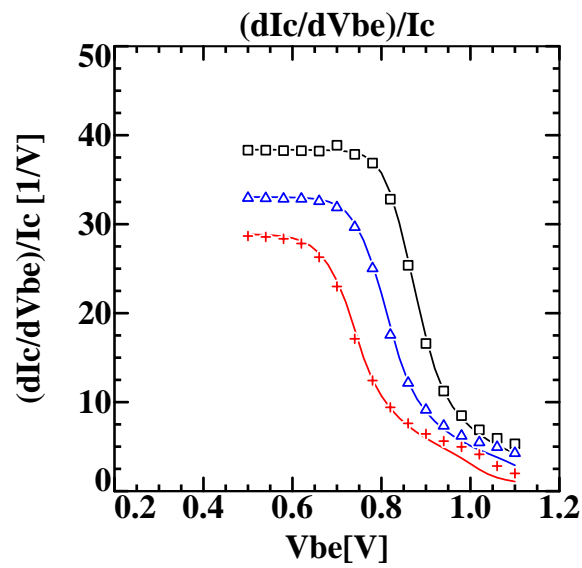
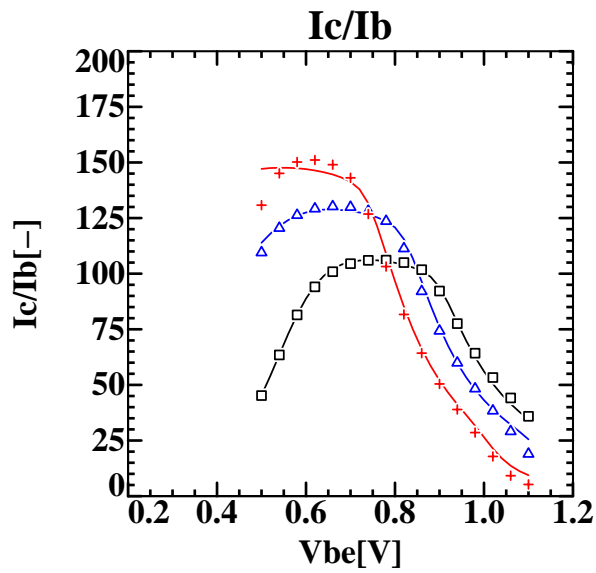
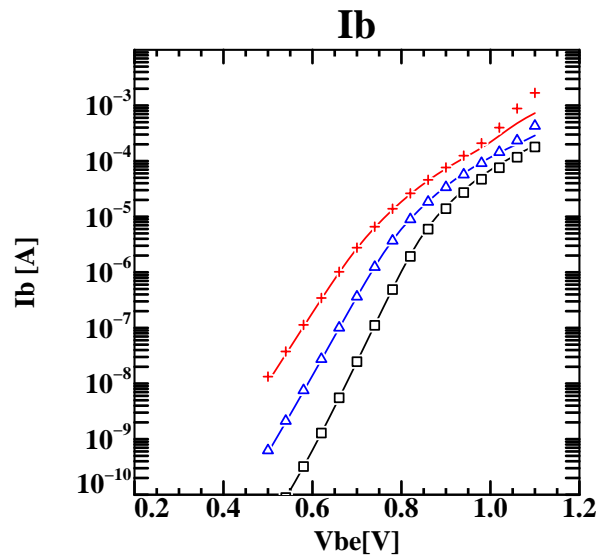
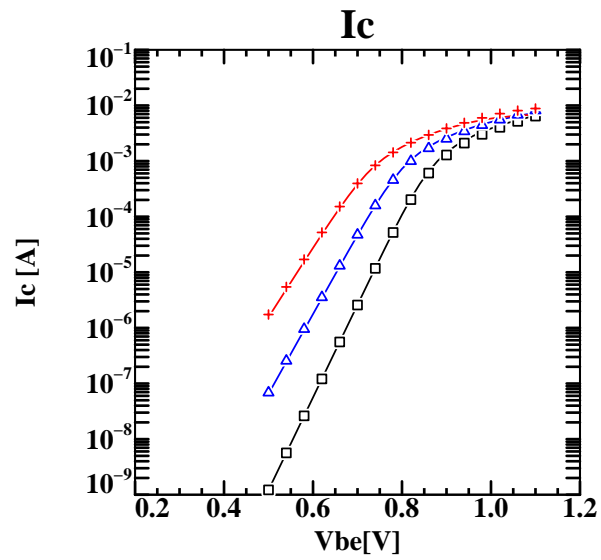
Emitter size:  $1.0 \times 5.0 \mu\text{m}^2$

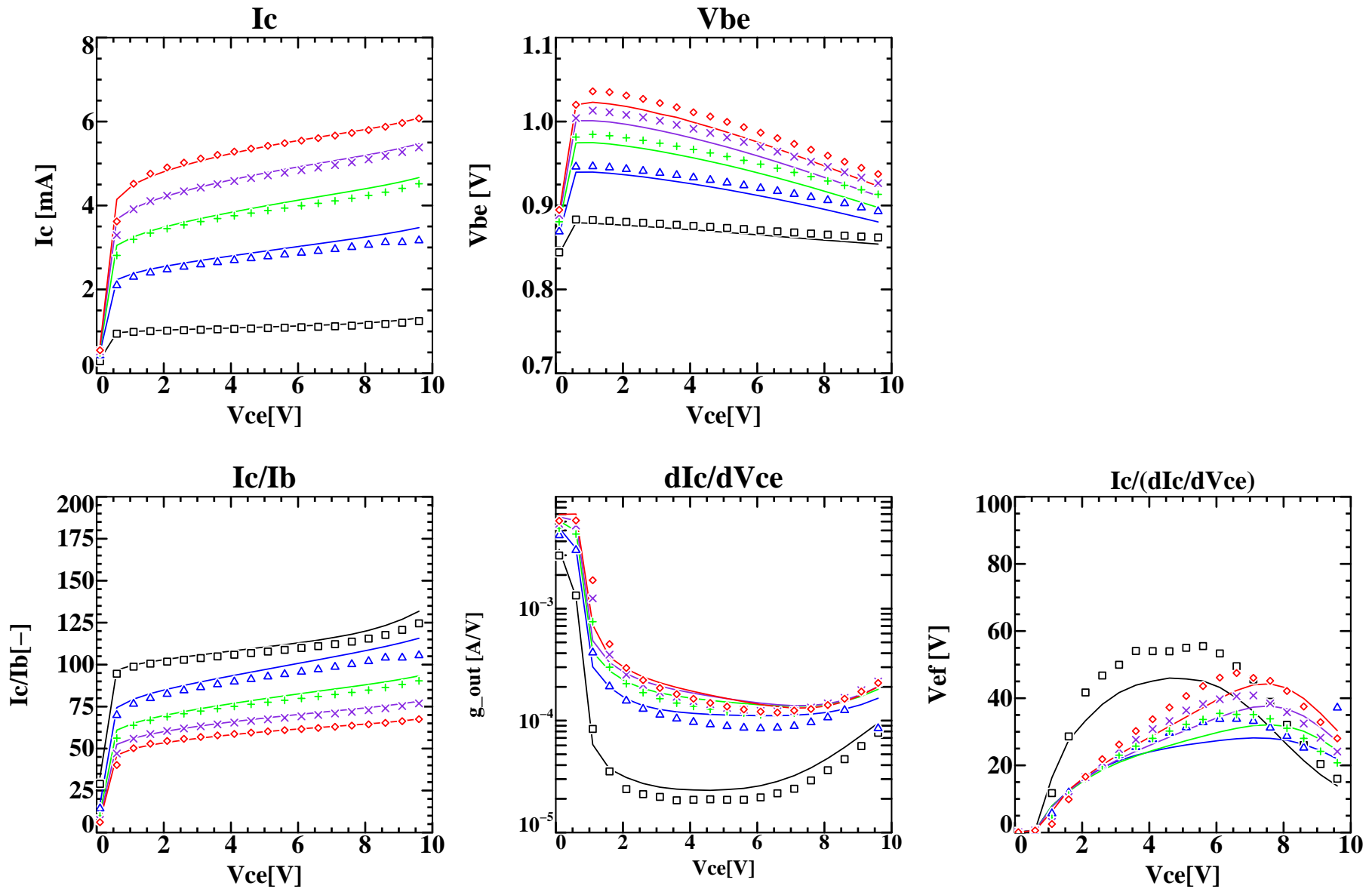
Base:  $\rho_{\square} = 12 \text{ k}\Omega$

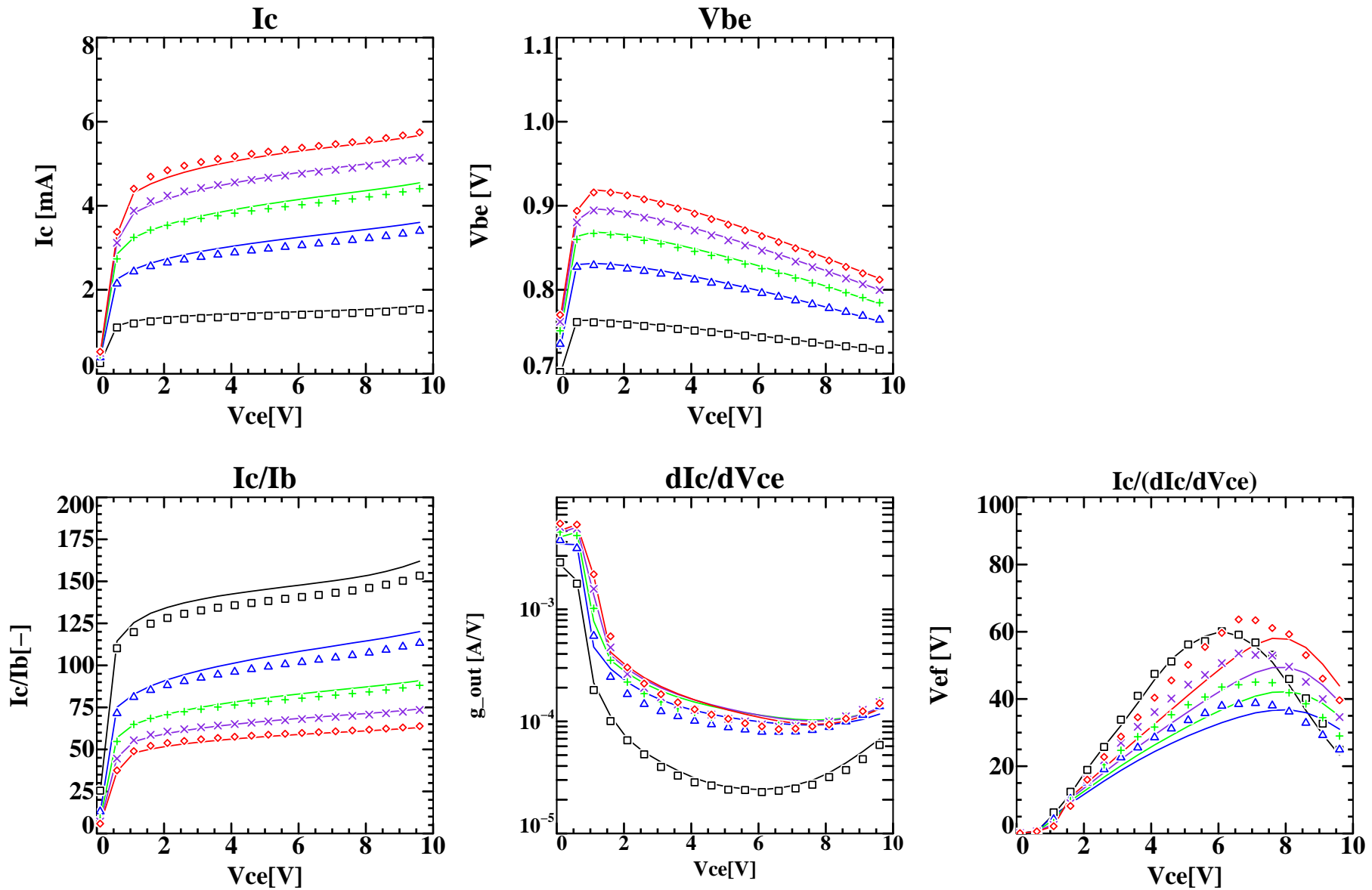
Maximum cut off frequency  $f_T$

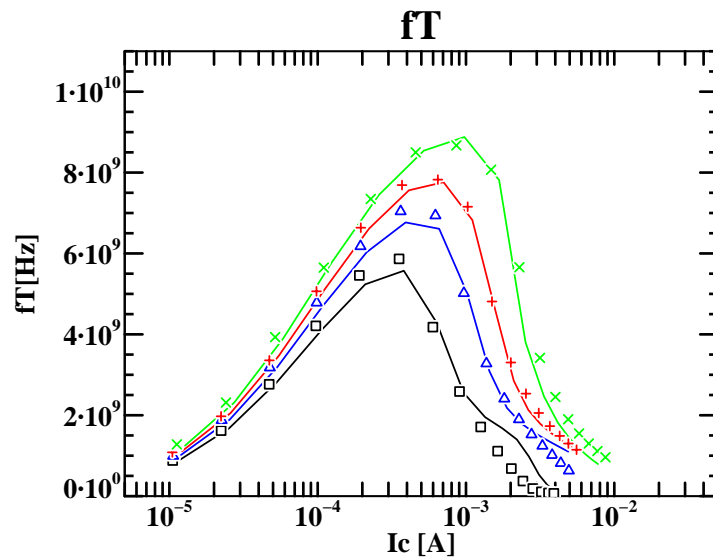
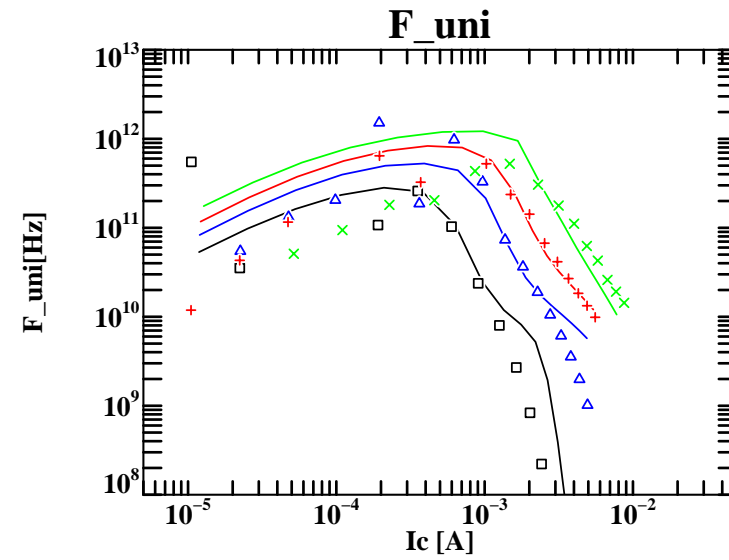
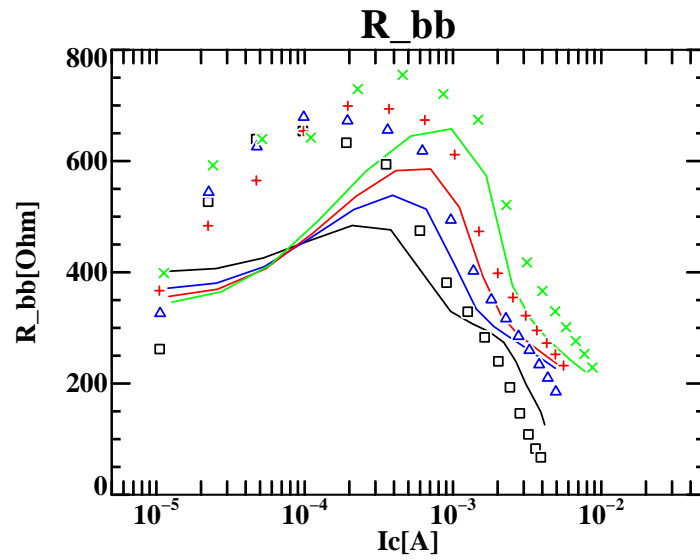
$f_T$ : 10 GHz @  $V_{CE} = 6 \text{ V}$

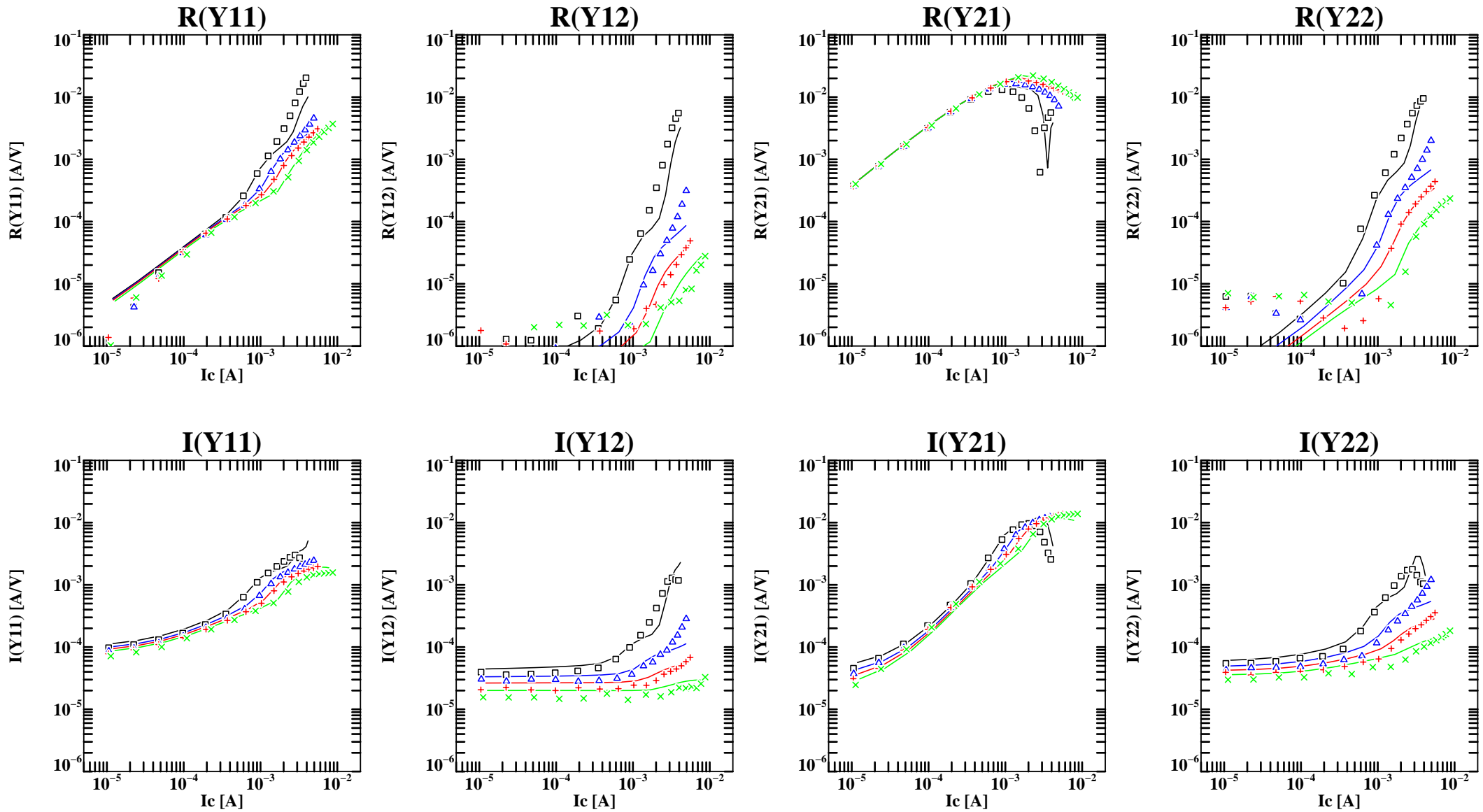
Problem for modeling: self-heating ( $R_{th} = 2500^{\circ}\text{C}/\text{W}$ )











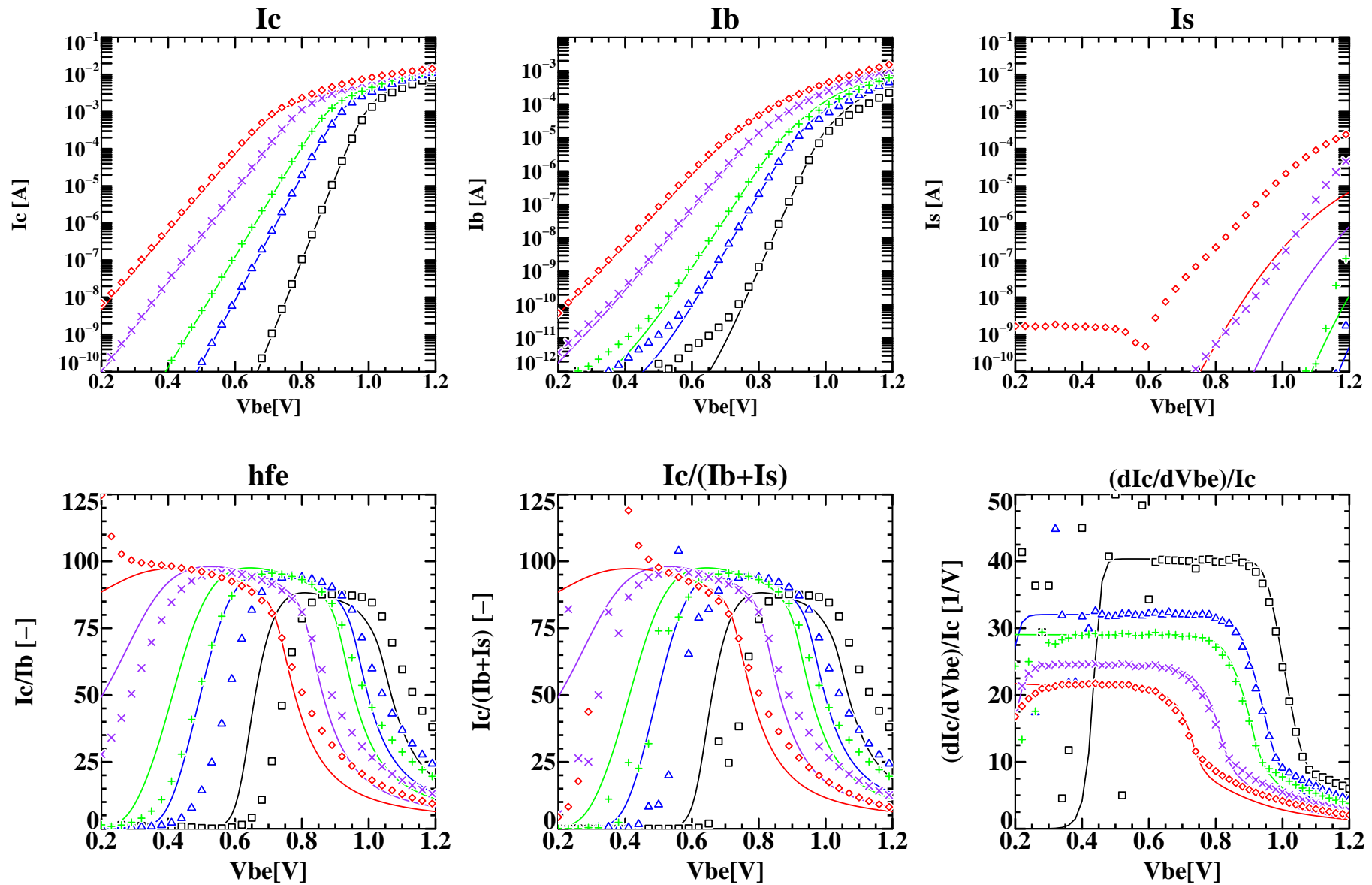
Double Poly BiCMOS process

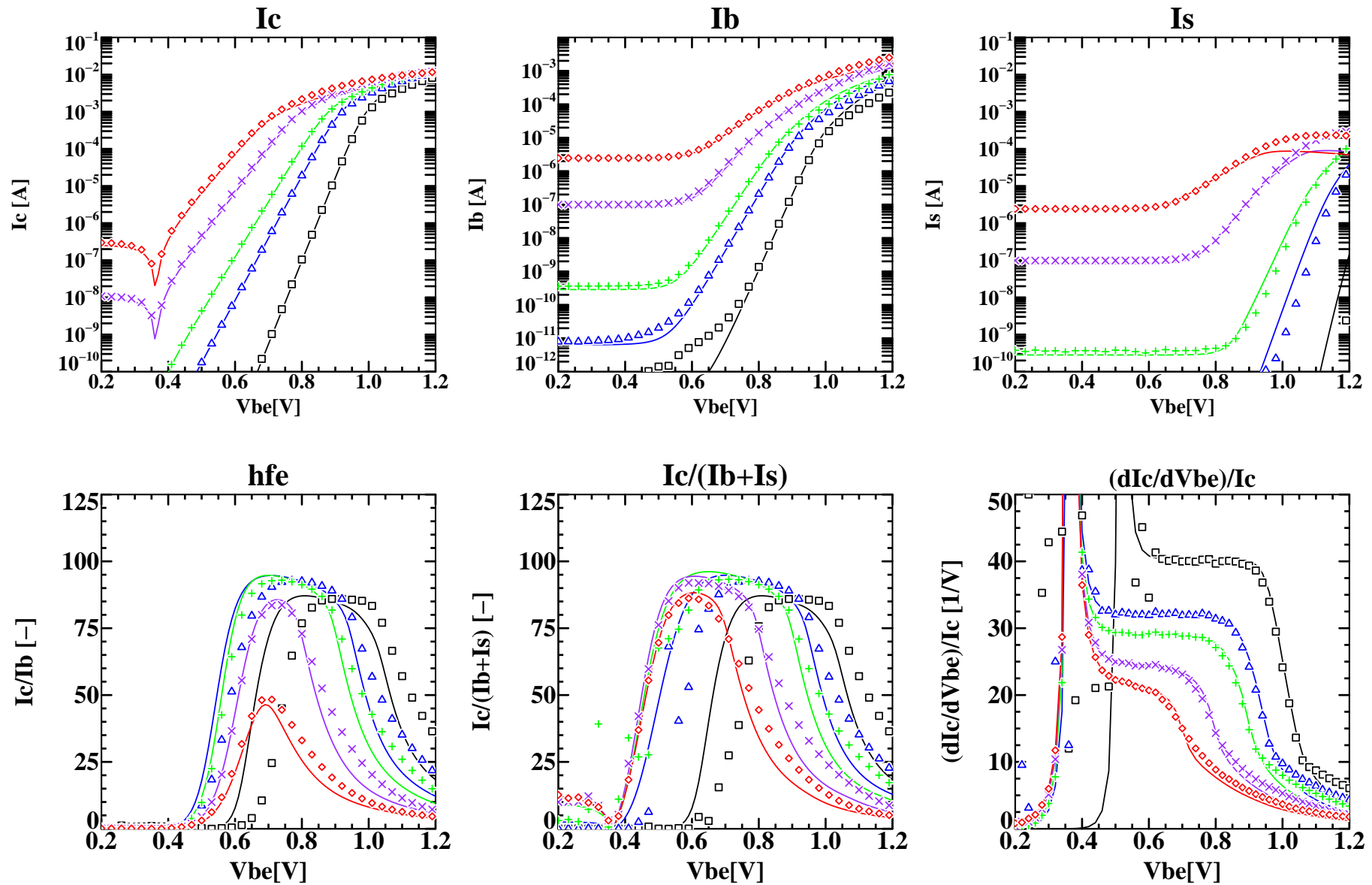
Emitter size:  $0.55 \times 3.45 \mu\text{m}^2$

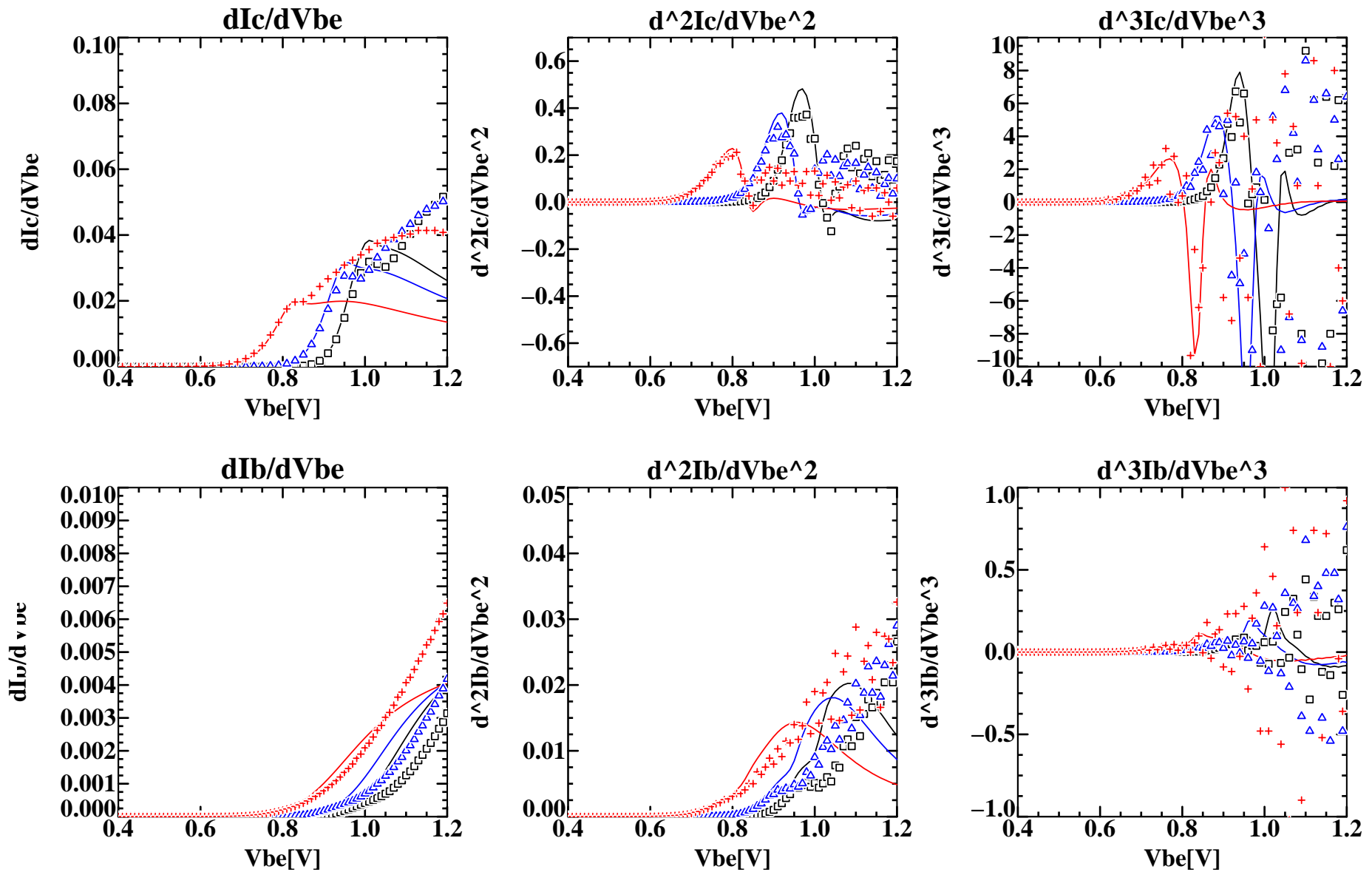
Single base contact:  $\rho_{\square} = 9 \text{ k}\Omega$

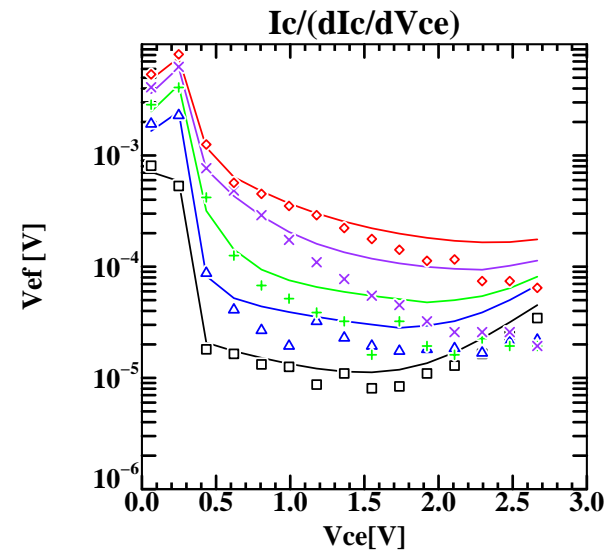
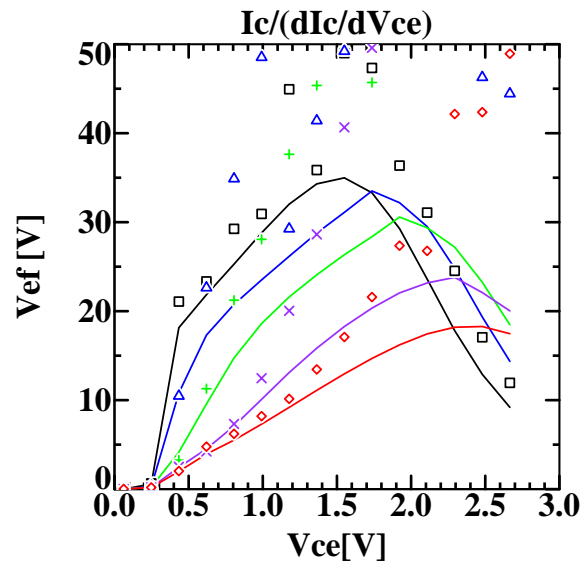
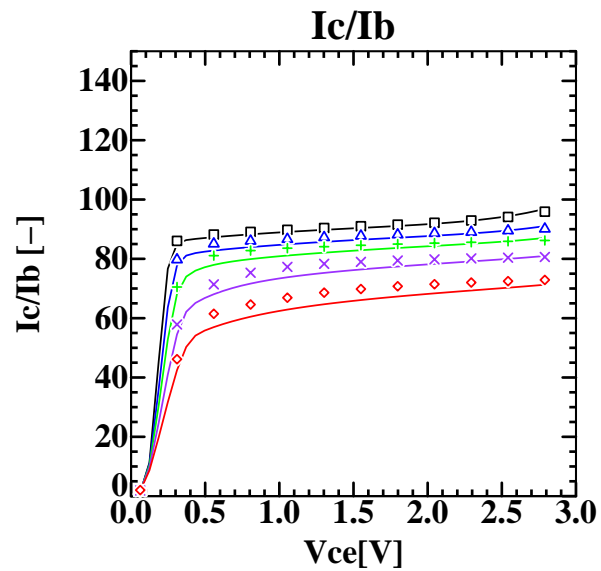
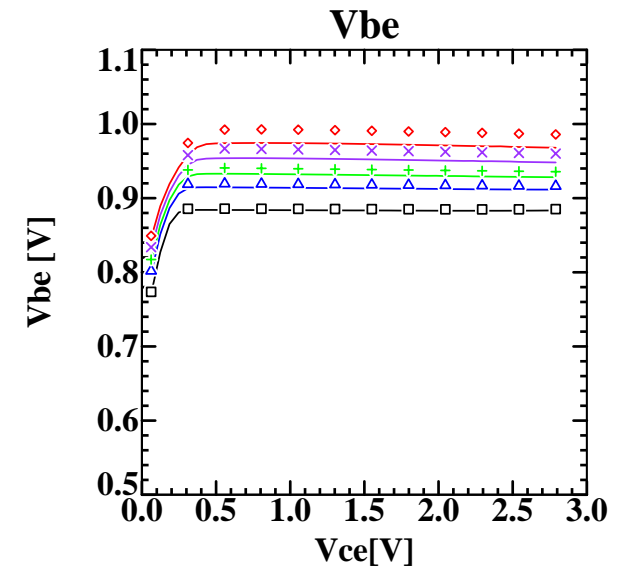
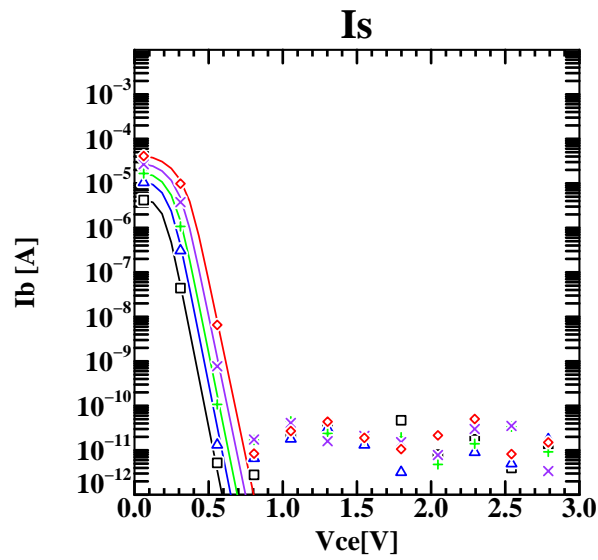
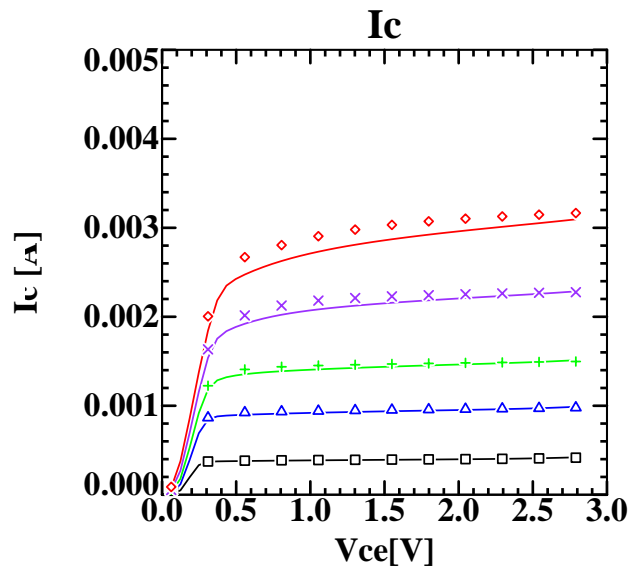
Maximum cut off frequency  $f_T$

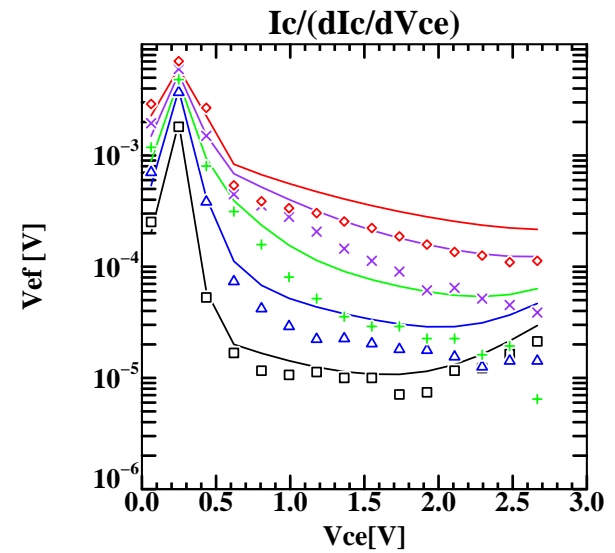
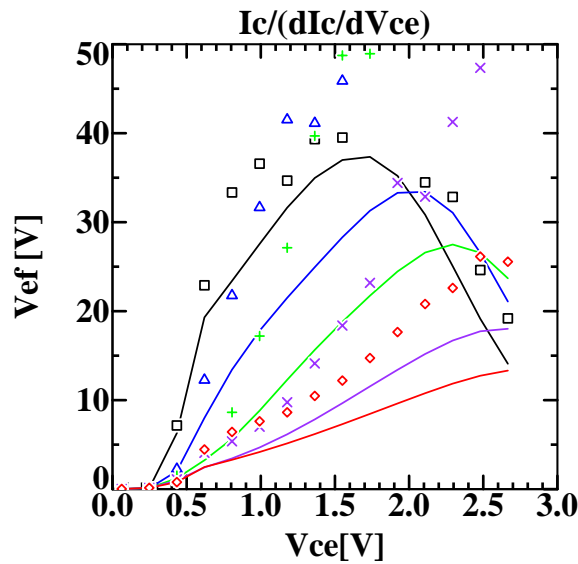
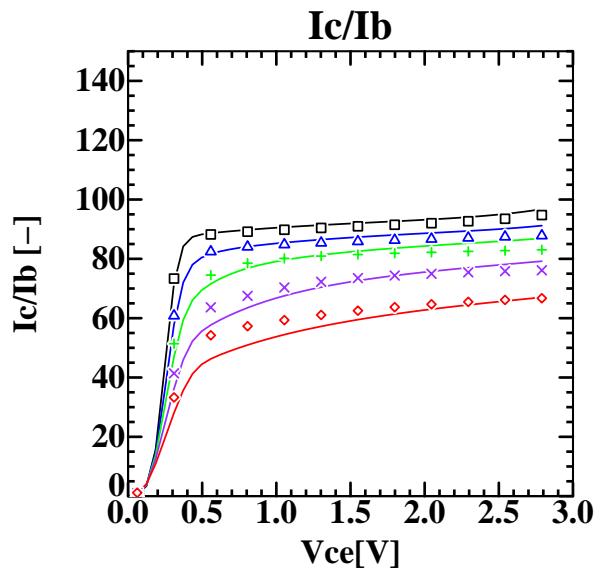
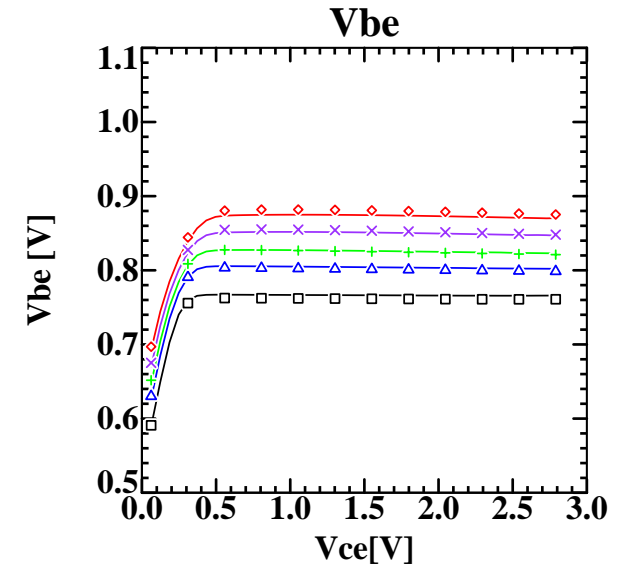
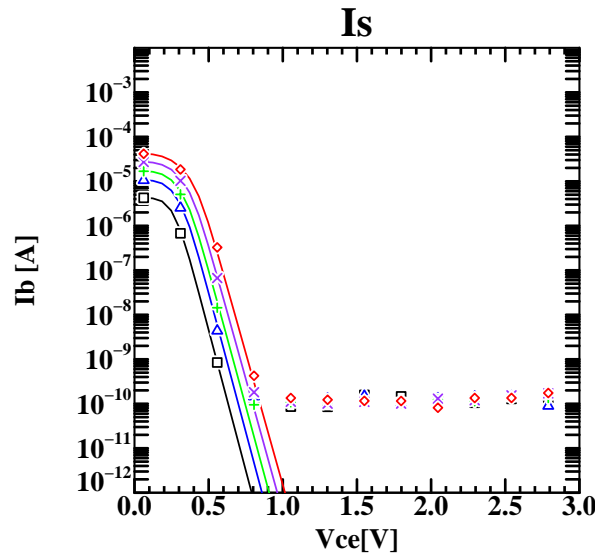
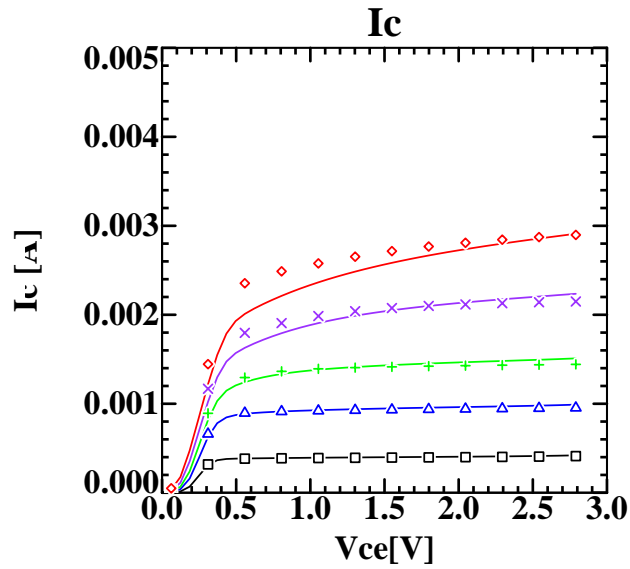
$f_T$ : 25 GHz @  $V_{CE} = 1 \text{ V}$

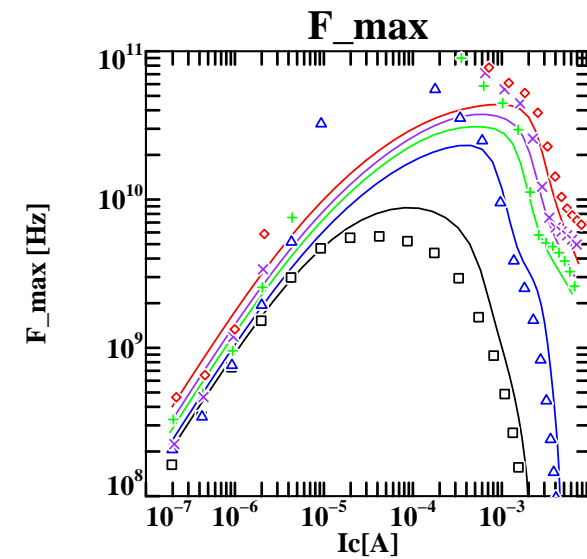
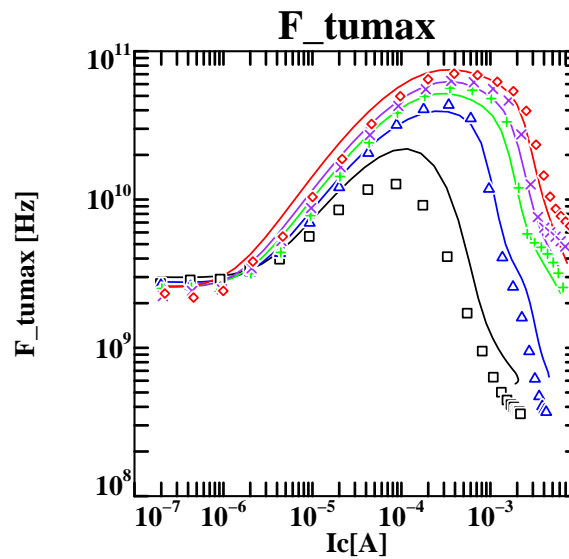
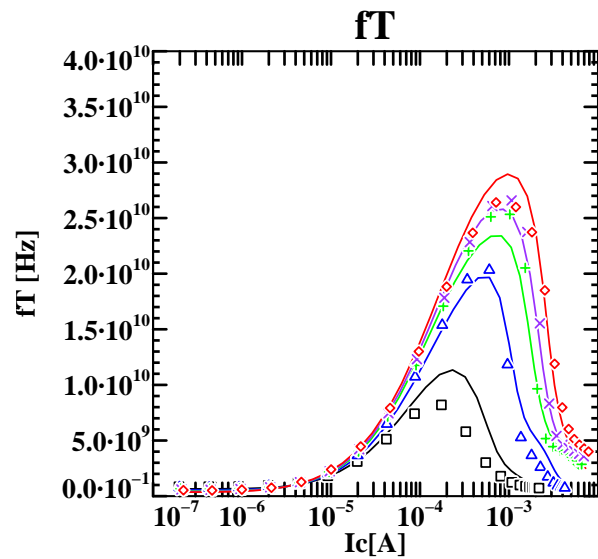
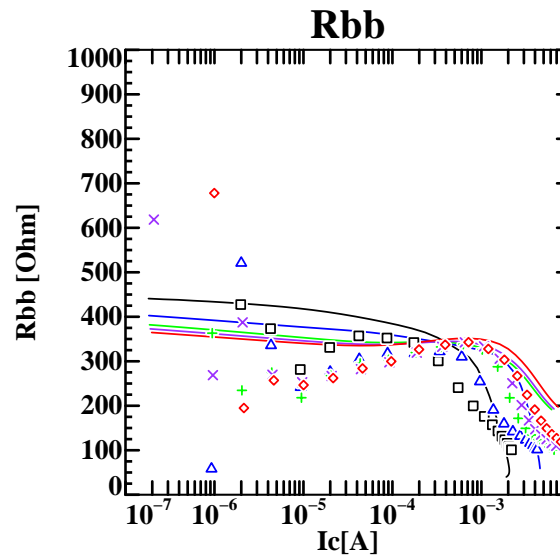
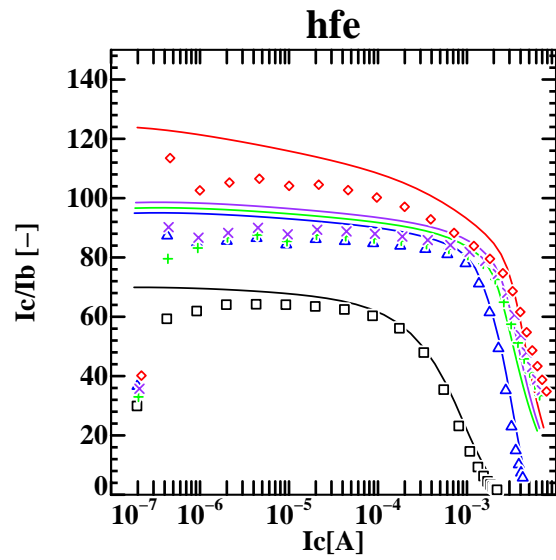


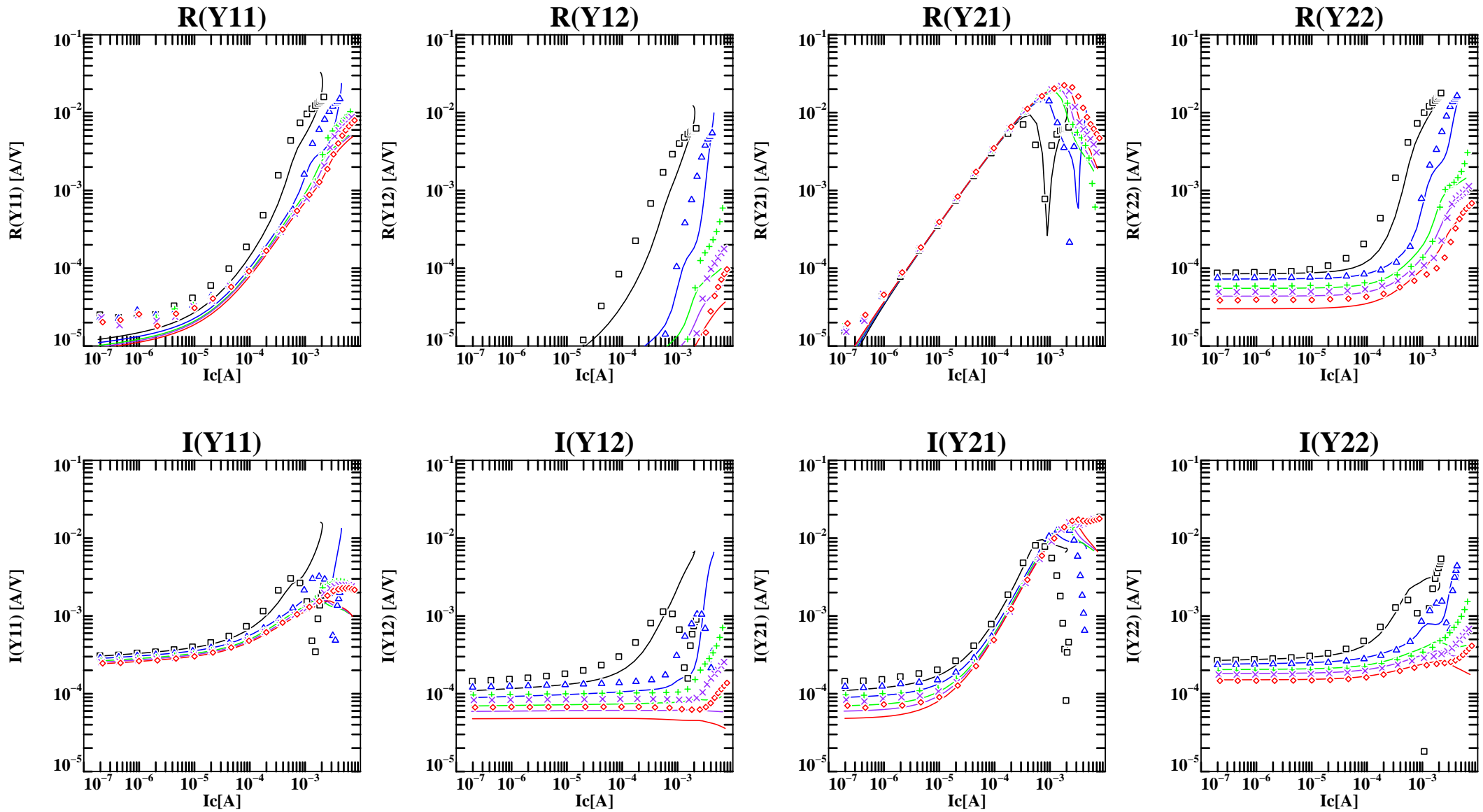












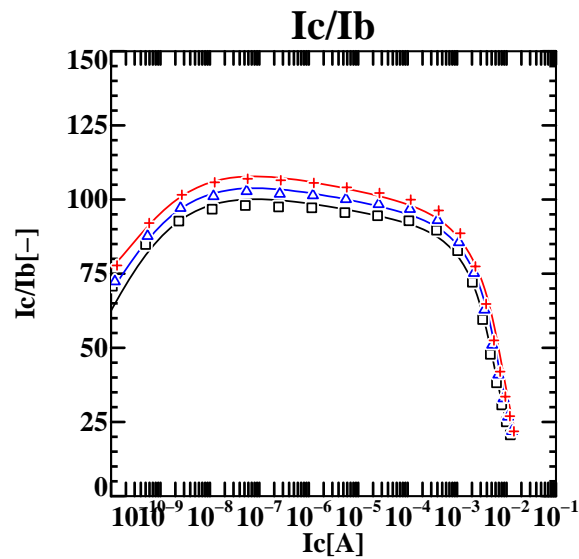
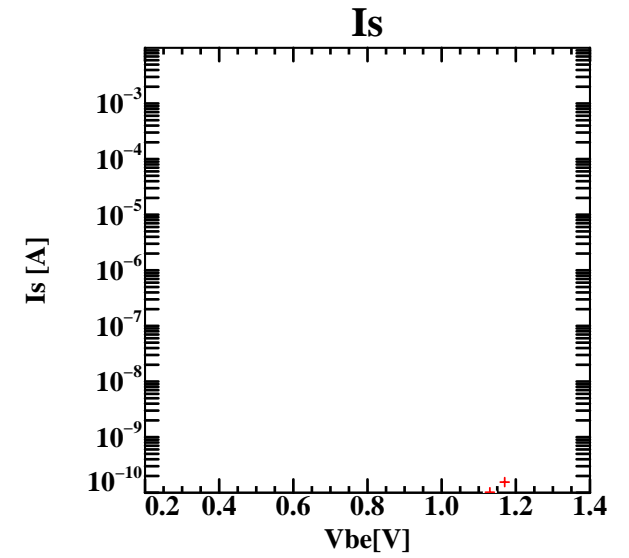
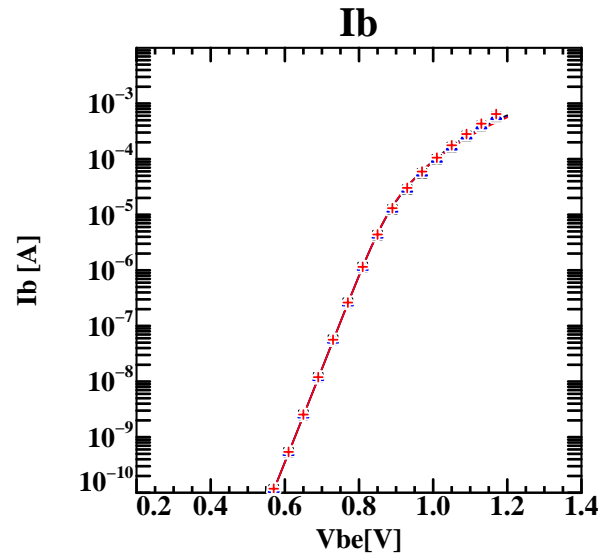
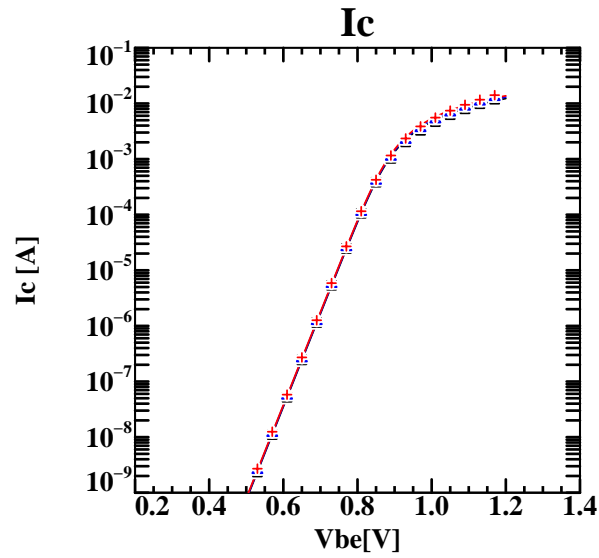
Si BiCMOS process

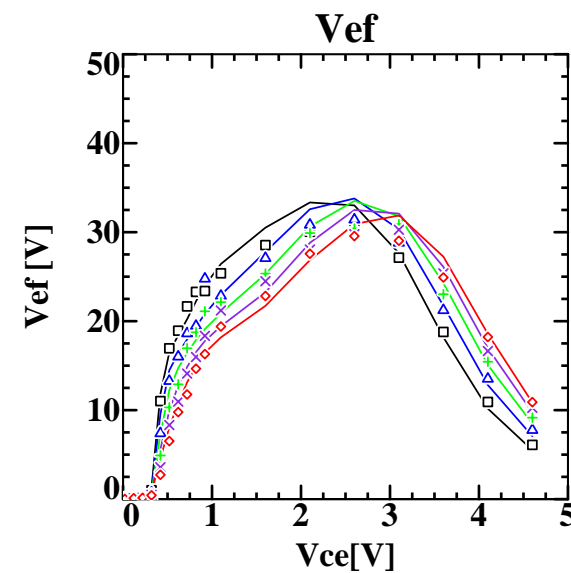
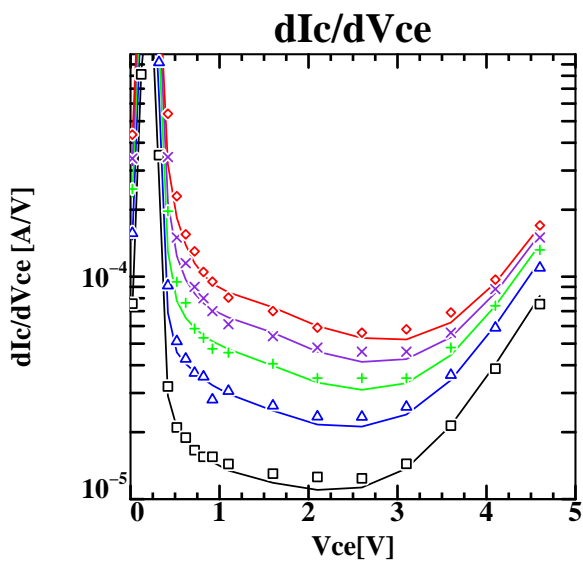
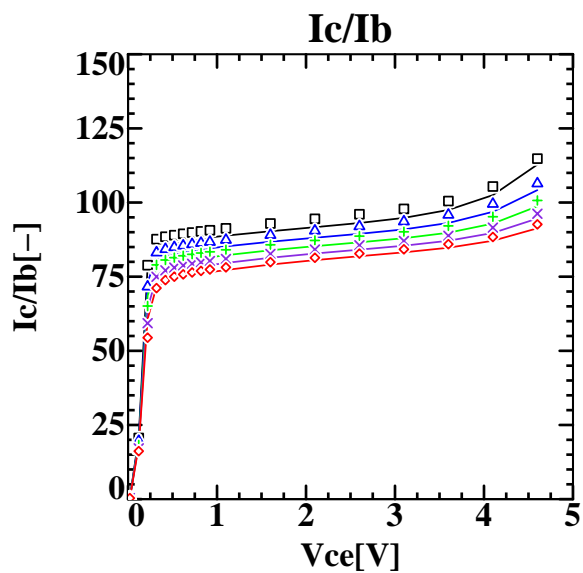
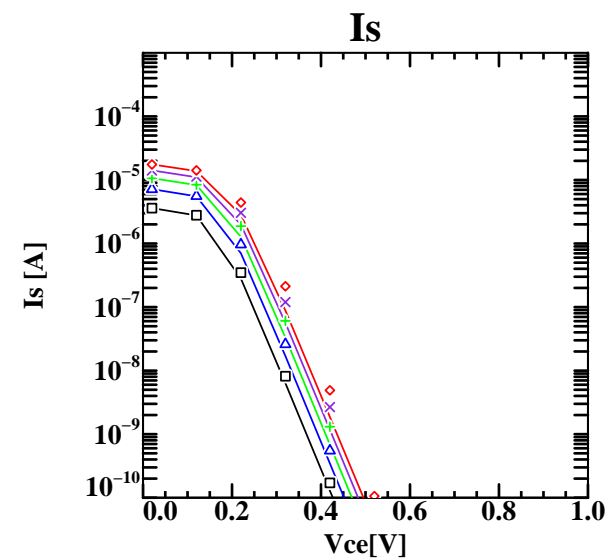
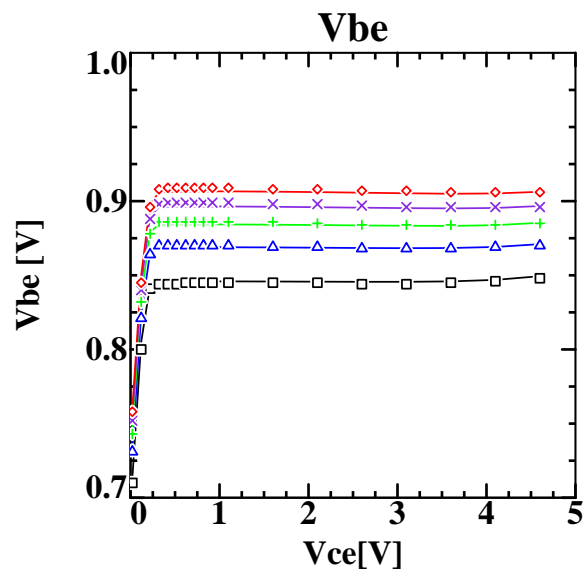
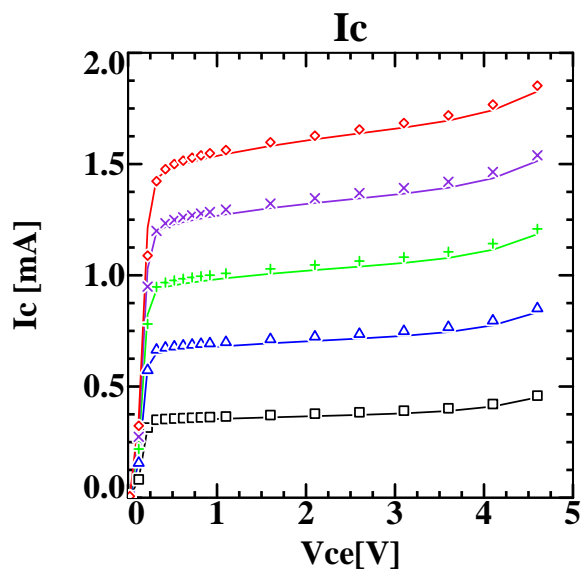
Emitter size:  $0.6 \times 4.8 \mu\text{m}^2$

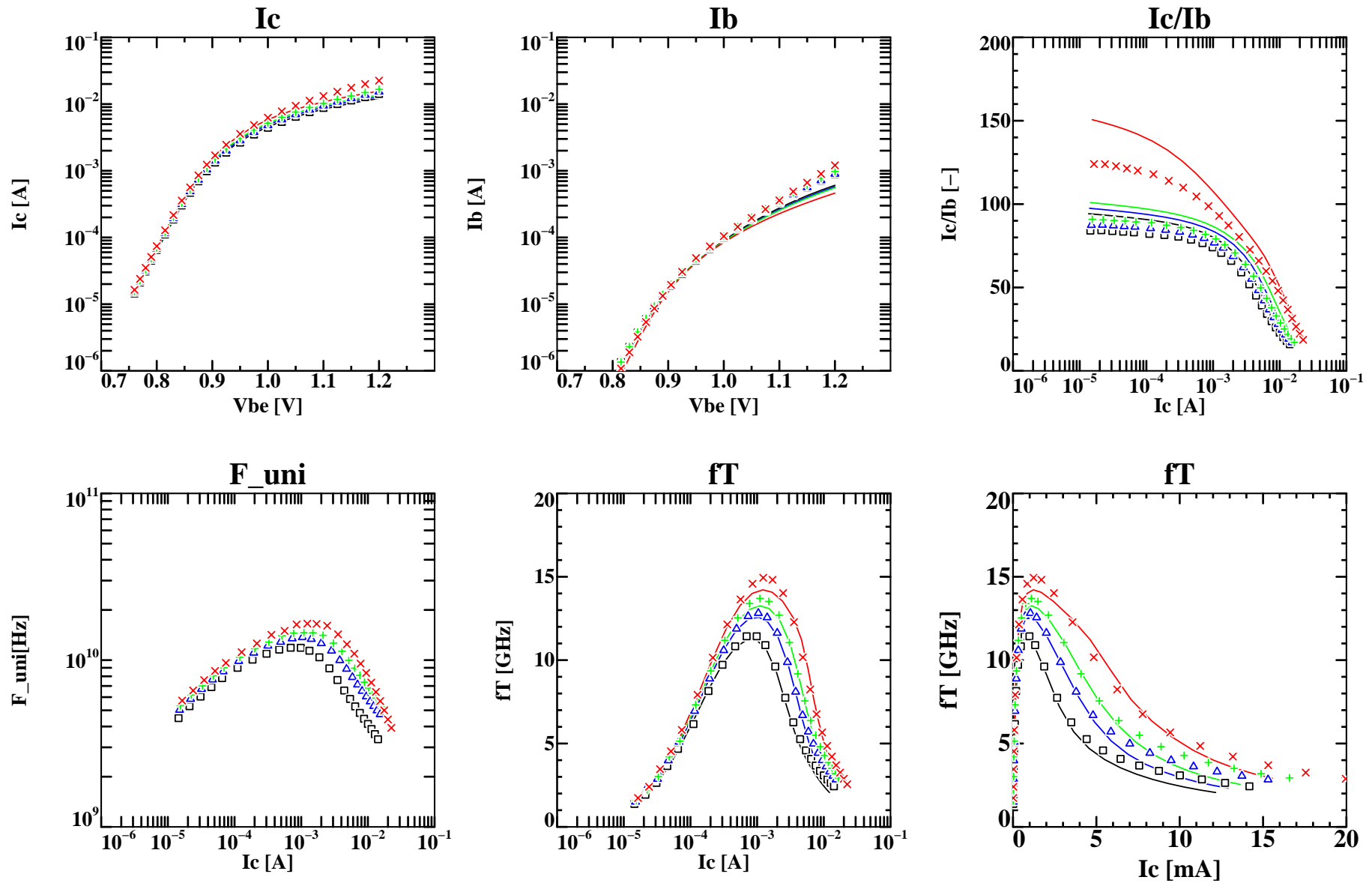
Base:  $\rho_{\square} = 10 \text{ k}\Omega$

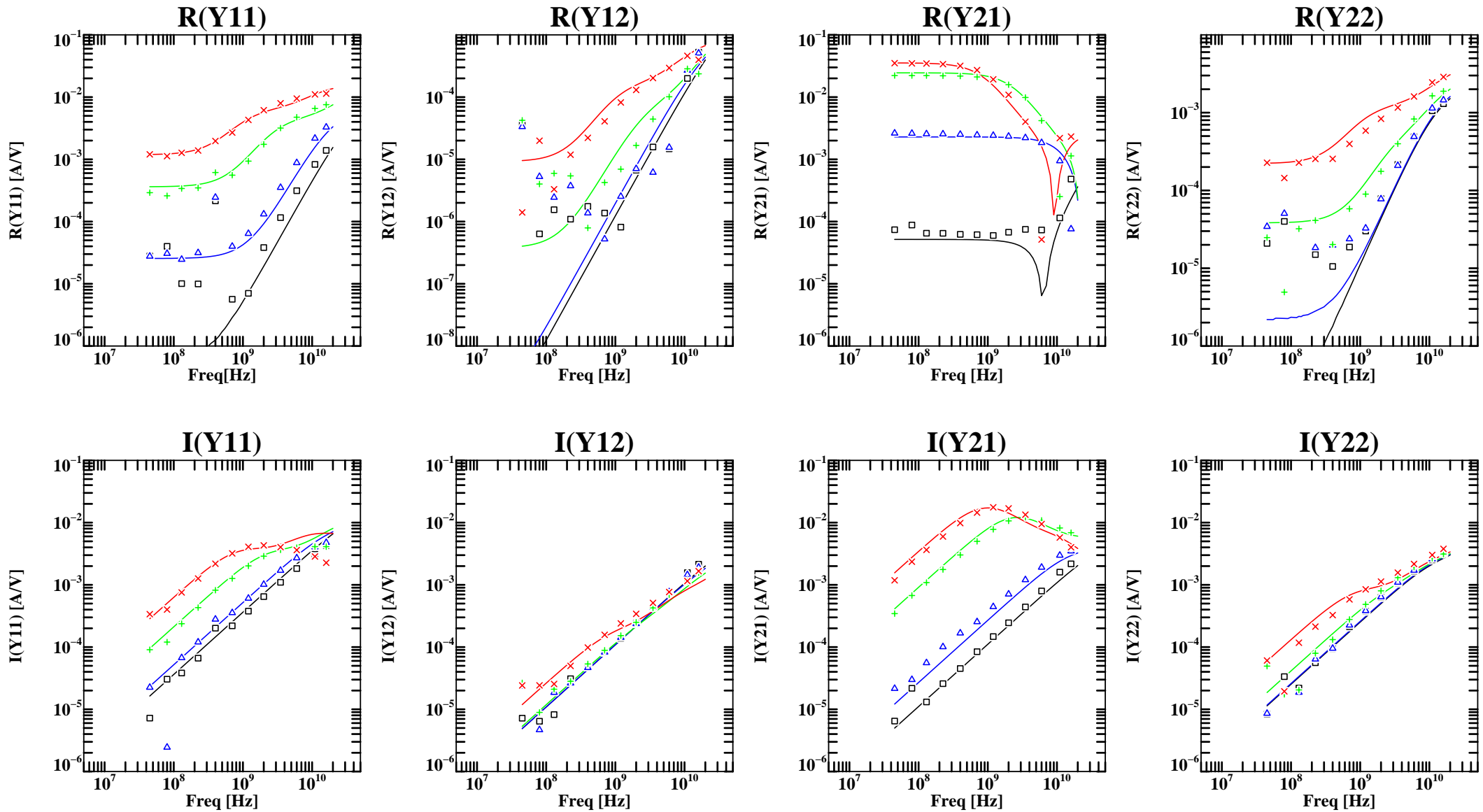
Maximum cut off frequency  $f_T$

$f_T$ : 25 GHz @  $V_{CB} = 4 \text{ V}$









SiGe process

Maximum cut off frequency  $f_T$

$f_T$ : 50 GHz @  $V_{CB} = 1.5$  V

