

## Functional selection guide

## ABT family

## GATES

FUNCTION		DEVICE NUMBER
<b>INVERTERS</b>	Hex inverter	74ABT04
<b>NAND</b>	Quad 2 input	74ABT00
	Triple 3 input	74ABT10
	Dual 4 input	74ABT20
<b>AND</b>	Quad 2 input	74ABT08
<b>NOR</b>	Quad 2 input	74ABT02
<b>OR</b>	Quad 2 input	74ABT32

## FLIP FLOPS

FUNCTION	DEVICE NUMBER	CLOCK EDGE	SET	RESET	OUTPUT
Dual D-type	74ABT74	positive	LOW	LOW	NINV, INV
Octal D-type	74ABT273A	positive	–	LOW	NINV
Octal D-type, 3-state	74ABT374A	positive	–	–	NINV
Octal D-type, with enable	74ABT377A	positive	–	–	NINV
Octal D-type, 3-state	74ABT534A	positive	–	–	INV
Octal D-type, 3-state	74ABT574A	positive	–	–	NINV
10-bit D-type, 3-state	74ABT821	positive	–	–	NINV
9-bit D-type, with reset and enable, 3-state	74ABT823	positive	–	LOW	NINV

## LATCHES

FUNCTION	DEVICE NUMBER	RESET LEVEL	ENABLE LEVEL	OUTPUT
Octal transparent latch, 3-state	74ABT373A	LOW	HIGH	NINV
Octal D-type transparent latch, 3-state	74ABT573A	LOW	HIGH	NINV
10-bit bus interface latch, 3-state	74ABT841	LOW	HIGH	NINV

## REGISTERS

FUNCTION	DEVICE NUMBER	BITS	LATCH ENABLE	OUTPUT
Octal register transceiver with dual enable, 3-state	74ABT543A	8	LOW	NINV
Octal register transceiver with dual enable, inverting, 3-state	74ABT544	8	LOW	INV
Octal bus transceiver/register, 3-state	74ABT646A	8	–	NINV
Octal bus transceiver/register, inverting, 3-state	74ABT648	8	–	INV
Octal transceiver/register, inverting, 3-state	74ABT651	8	–	INV
Octal transceiver/register, 3-state	74ABT652A	8	–	NINV
Octal registered transceiver, 3-state	74ABT2952	8	–	NINV

**BUFFERS, DRIVERS & TRANSCEIVERS**

FUNCTION	DEVICE NUMBER	ENABLE LEVEL	OUTPUT
Quad buffer/line driver, 3-state	74ABT125	LOW	NINV
Quad buffer/line driver, 3-state	74ABT126	HIGH	NINV
Octal inverting buffer, 3-state	74ABT240	2 LOW	INV
Octal inverting buffer with 30 $\Omega$ termination, 3-state	74ABT2240	2 LOW	INV
Octal buffer/line driver, 3-state	74ABT241	1 LOW + 1 HIGH	NINV
Octal buffer/line driver with 30 $\Omega$ termination, 3-state	74ABT2241	1 LOW + 1 HIGH	NINV
Octal buffer/line driver, 3-state	74ABT244	2 LOW	NINV
Octal buffer/line driver with 30 $\Omega$ termination, 3-state	74ABT2244	2 LOW	NINV
Octal transceiver with direction pin, 3-state	74ABT245	1 LOW	NINV
Octal transceiver with direction pin, 30 $\Omega$ termination, 3-state	74ABT2245	1 LOW	NINV
Octal buffer, inverting, 3-state	74ABT540	2 LOW	INV
Octal buffer/line driver, 3-state	74ABT541	2 LOW	NINV
Octal Transceiver with dual enable, inverting, 3-state	74ABT620	1 LOW + 1 HIGH	INV
Octal Transceiver with dual enable, non-inverting, 3-state	74ABT623	1 LOW + 1 HIGH	NINV
Octal transceiver with direction pin, inverting, 3-state	74ABT640	1 LOW	INV
Octal transceiver with parity generator/checker, 3-state	74ABT657	LOW	NINV
10-bit buffer/line driver, non-inverting, 3-state	74ABT827	2 LOW	NINV
Octal transceiver with parity generator/checker, 3-state	74ABT833	2 LOW	NINV
9-bit bus interface latch with set & reset, 3-state	74ABT843	LOW	NINV
9-bit bus transceiver, 3-state	74ABT863	4 LOW	NINV
9 bit dual latch transceiver with 8 bit parity checker/generator, 3-state	74ABT899	1 LOW + 1 HIGH	NINV